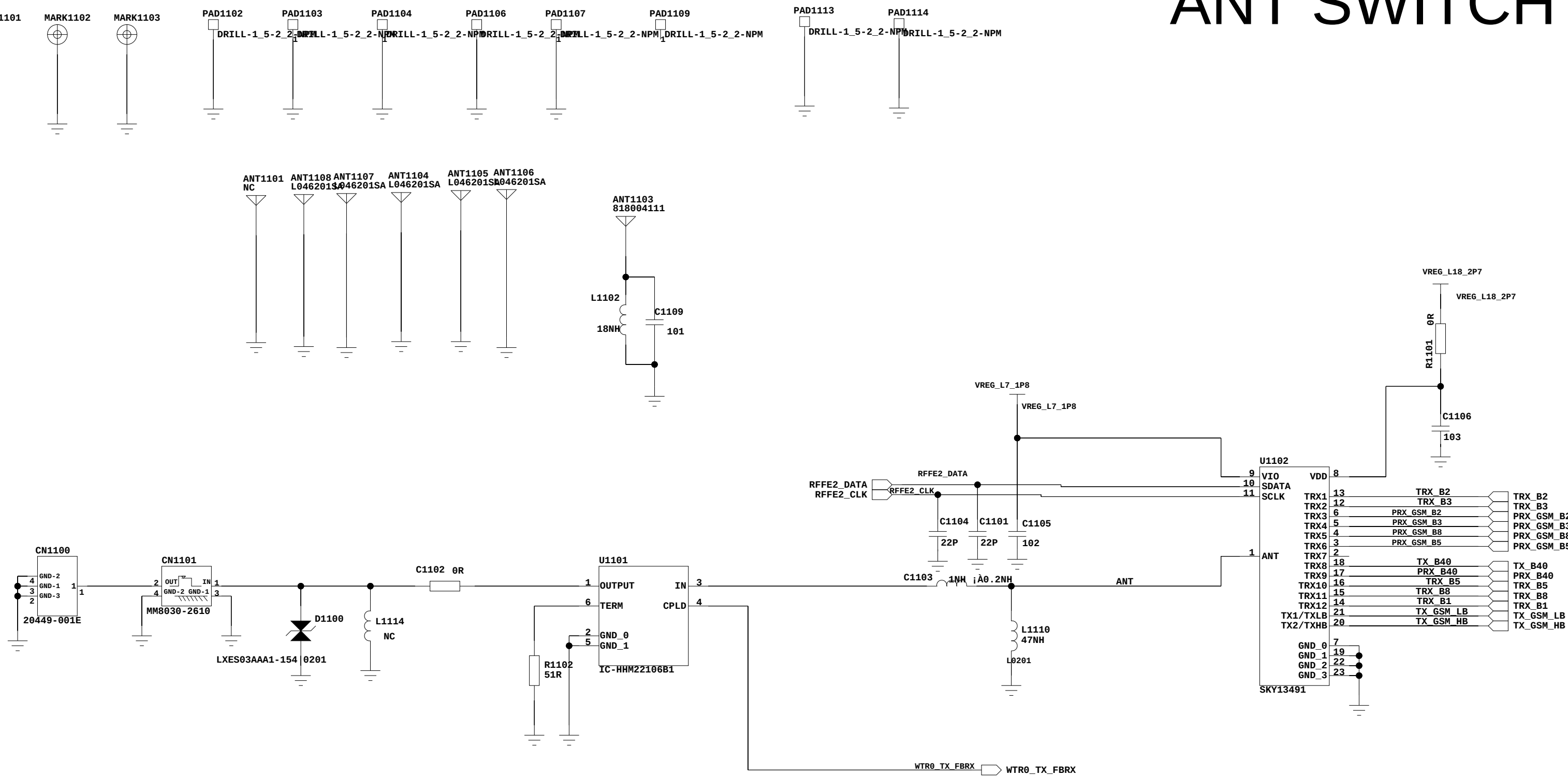
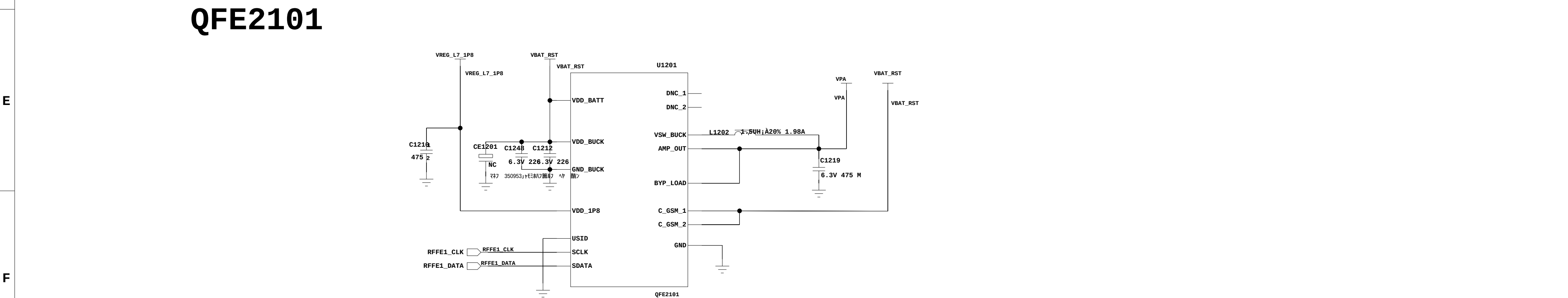
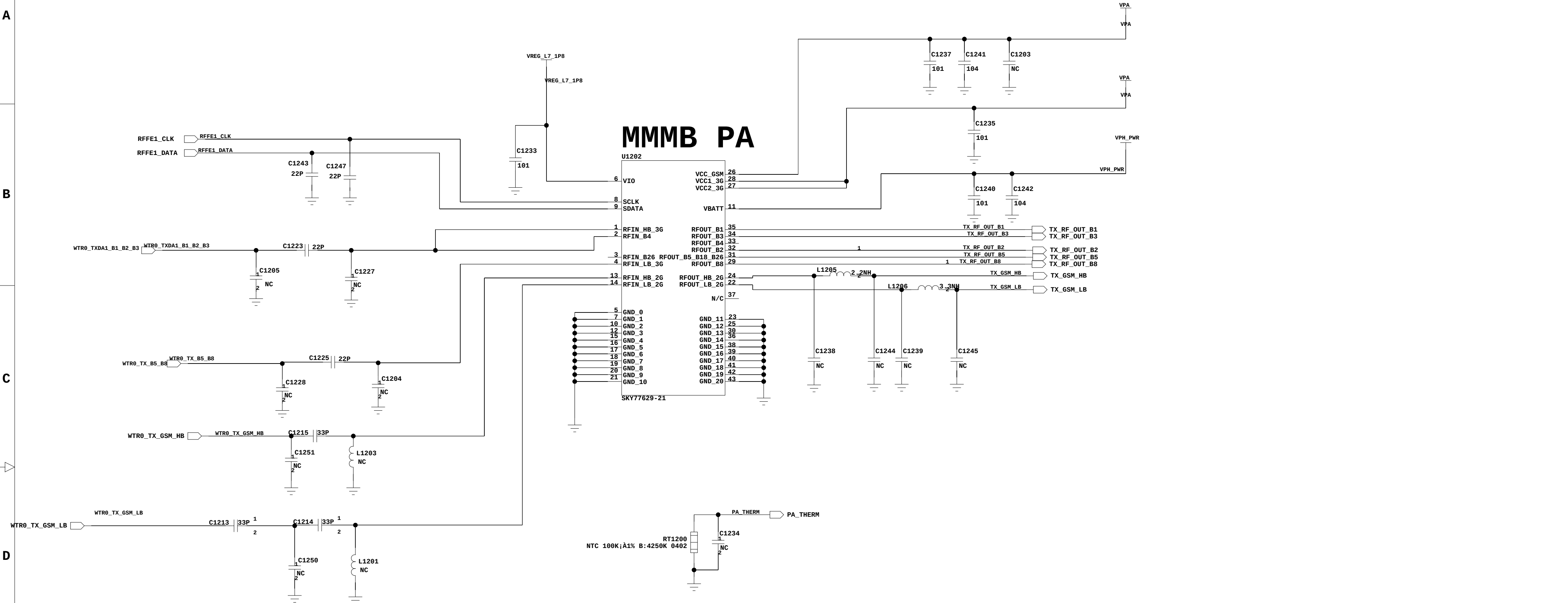


ANT SWITCH



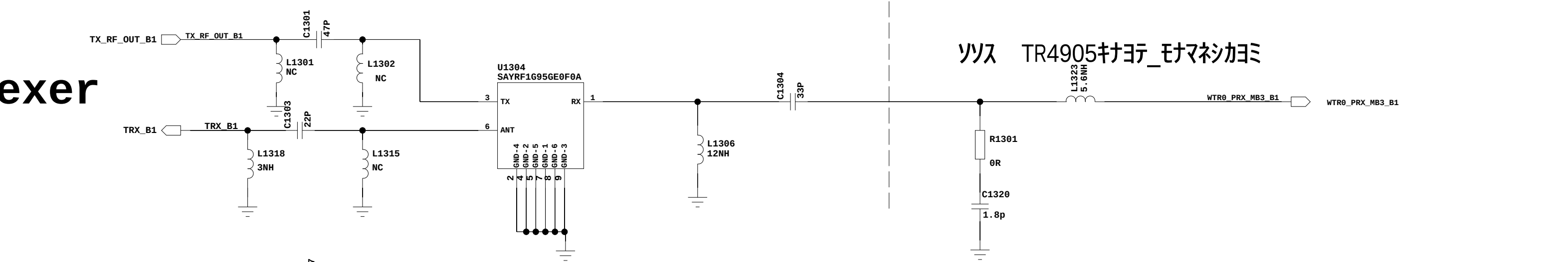
	Sign	Date	Type:
Drawn By			Draw No: Schematic1
Checked By			Sheet 1 of 20 Ver:
Standard By			
Approved By			





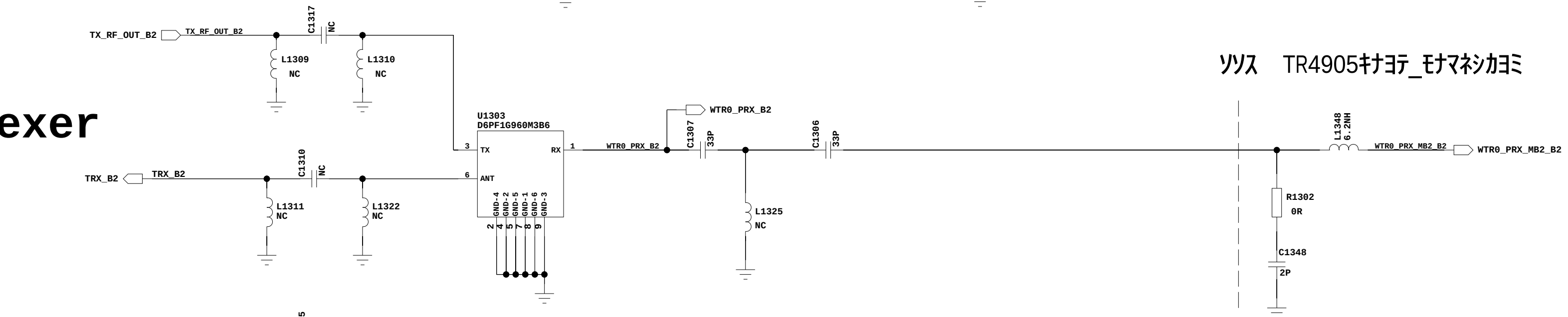
	Sign	Date	Type:		
Drawn By			Draw No: board1		
Checked By			Sheet	of	28 Ver:
Standard By					
Approved By					

B1 Duplexer



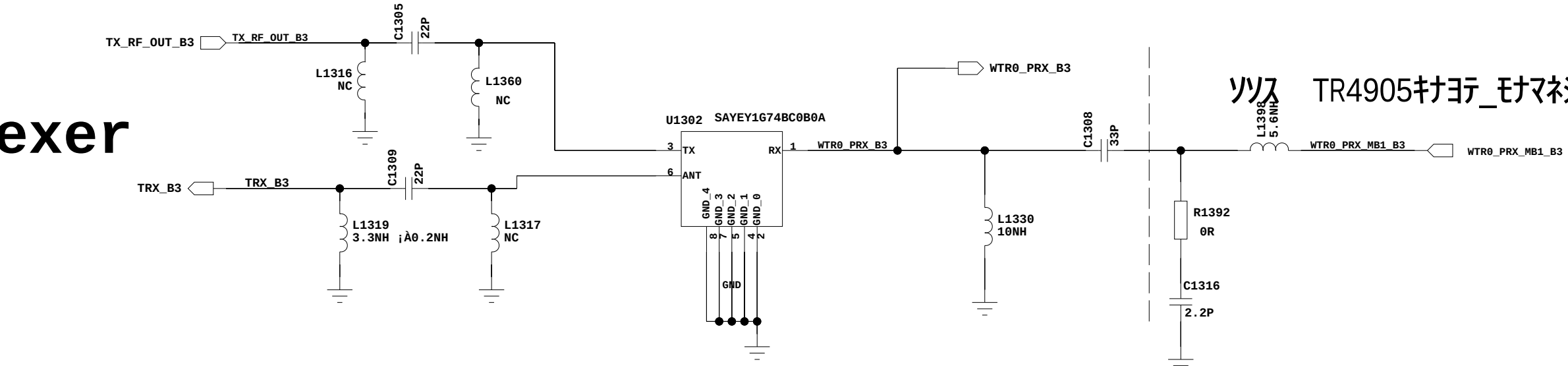
ソッス TR4905村ヨテ_モマネシカヨミ

B2 Duplexer



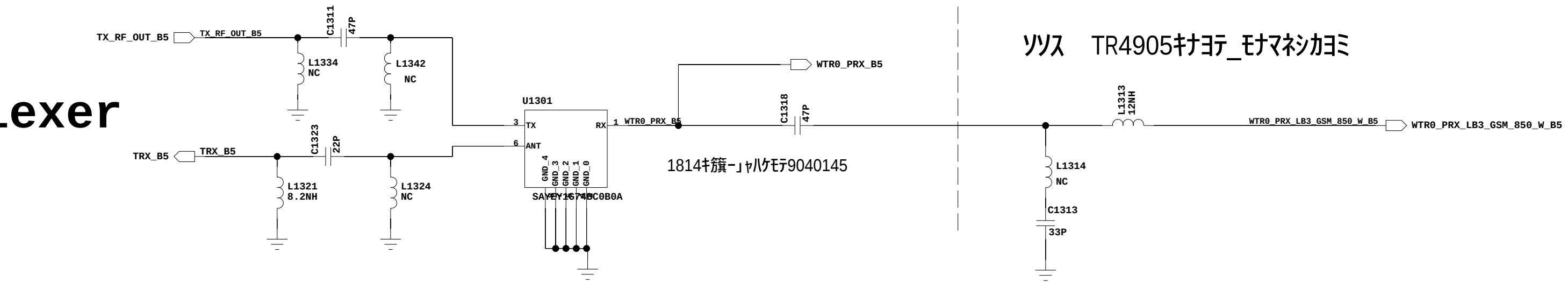
ソッス TR4905村ヨテ_モマネシカヨミ

B3 Duplexer



ソッス TR4905村ヨテ_モマネシカヨミ

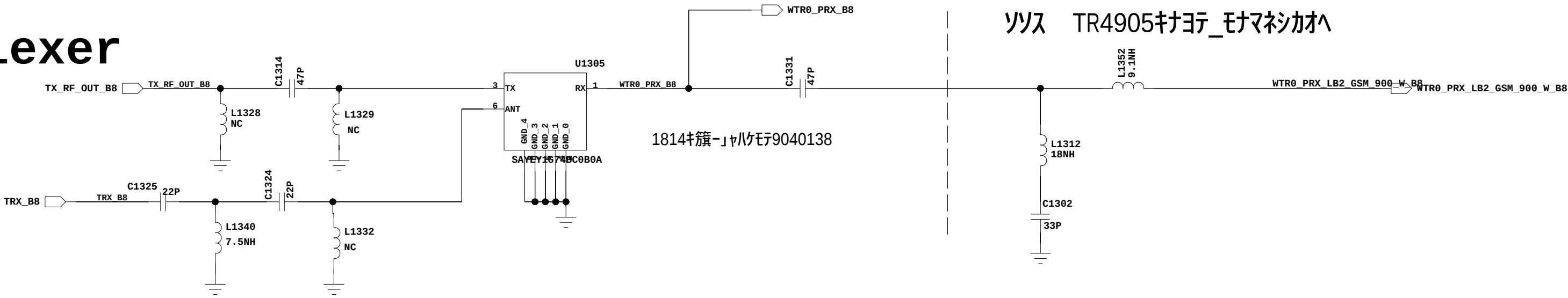
B5 Duplexer



ソッス TR4905村ヨテ_モマネシカヨミ

1814特旗-Jヤハケモテ9040145

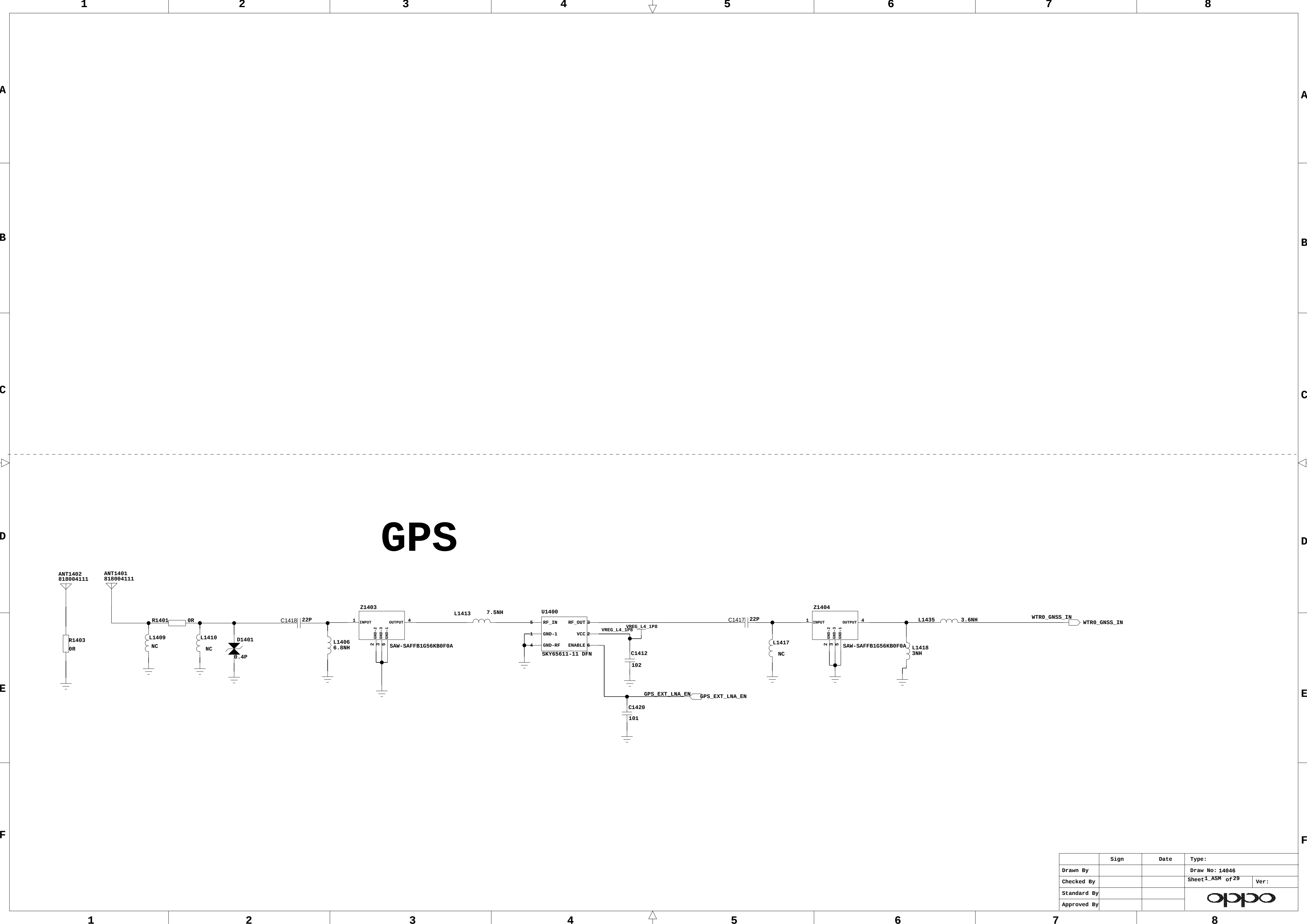
B8 Duplexer



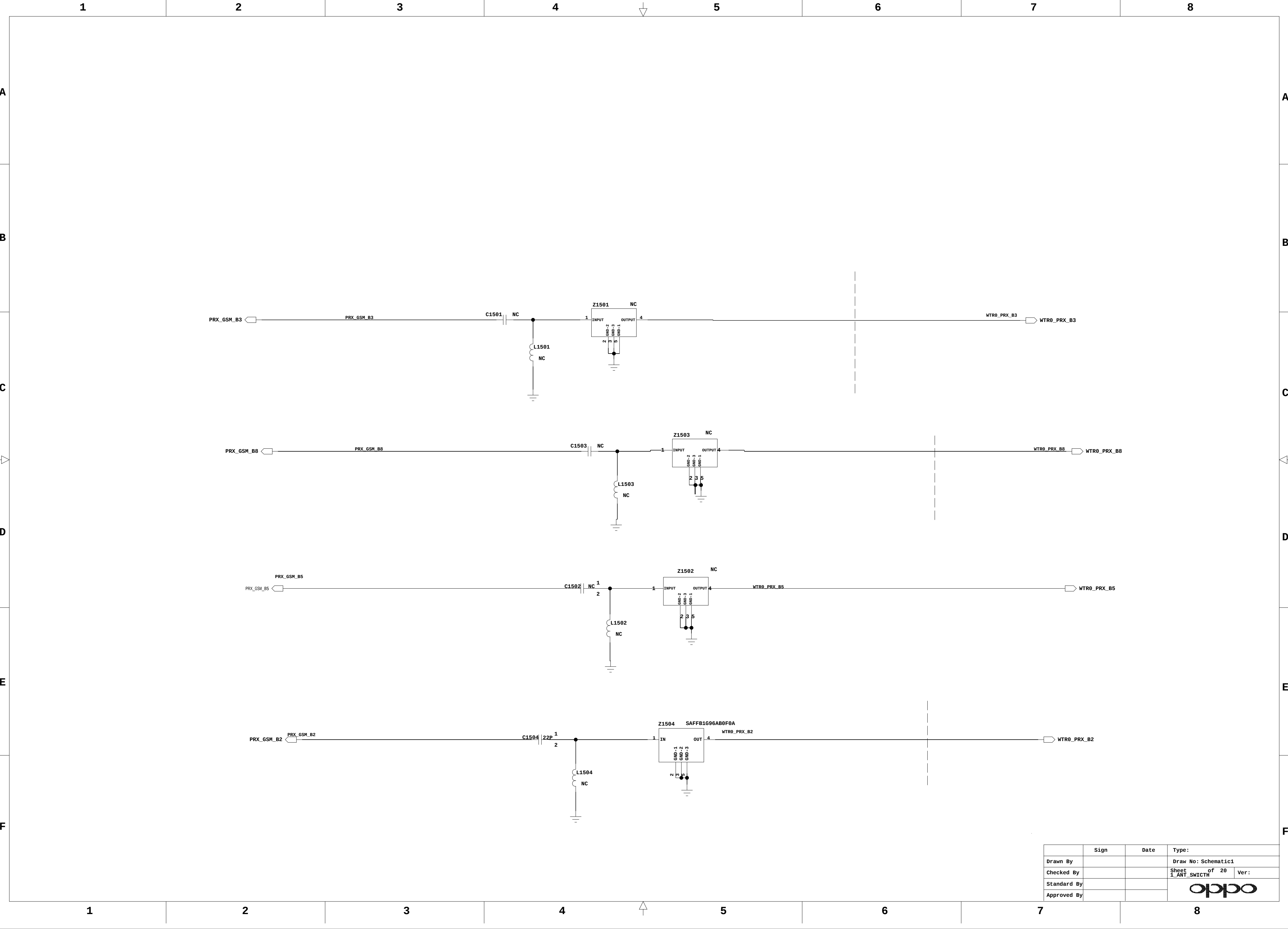
ソッス TR4905村ヨテ_モマネシカヨミ

1814特旗-Jヤハケモテ9040138

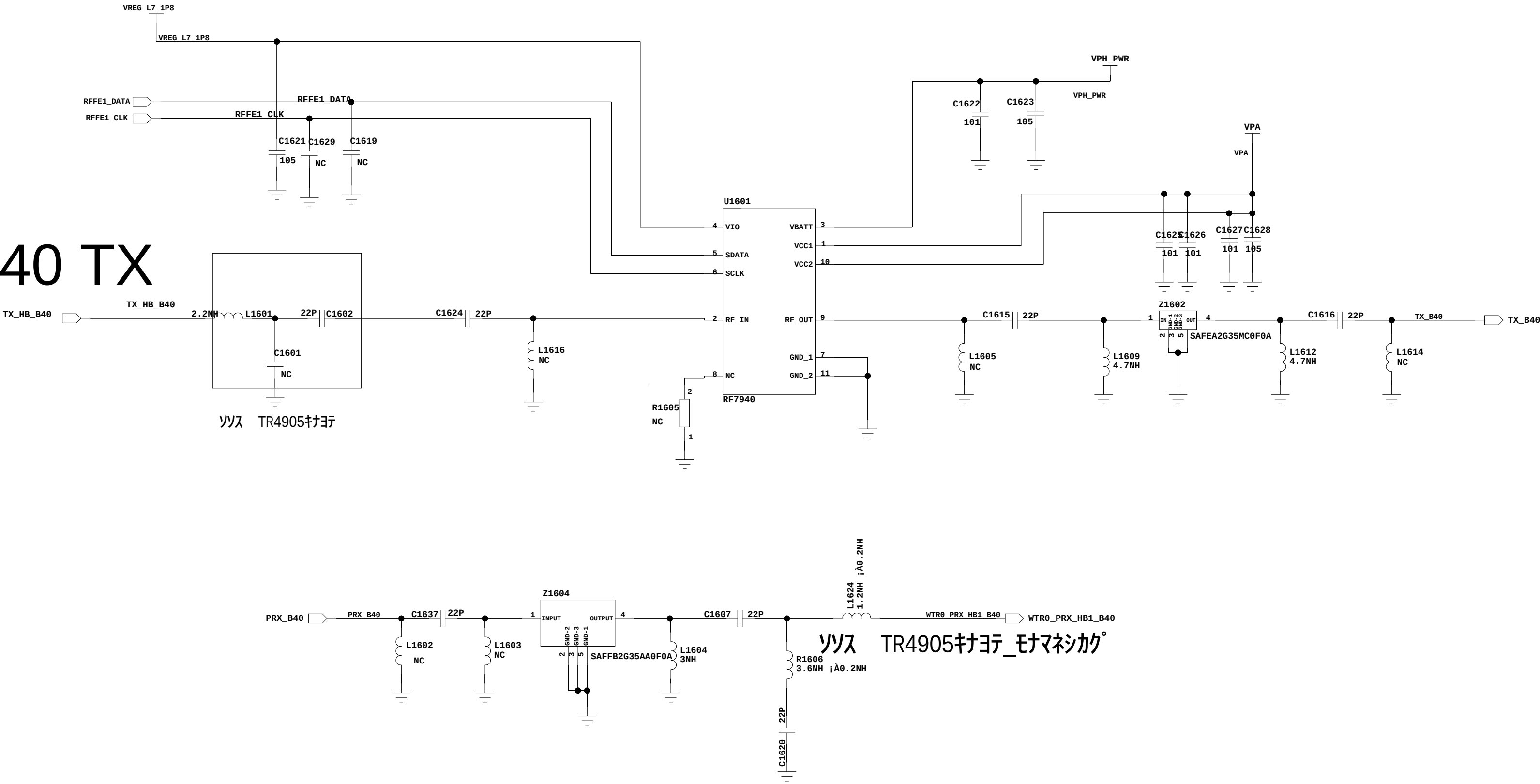
	Sign	Date	Type:
Drawn By			Draw No: 14046
Checked By			Sheet of 29 Ver:
Standard By			oppo
Approved By			



	Sign	Date	Type:	
Drawn By			Draw No: 14046	
Checked By			Sheet1_ASM of 29	Ver:
Standard By			oppo	
Approved By				

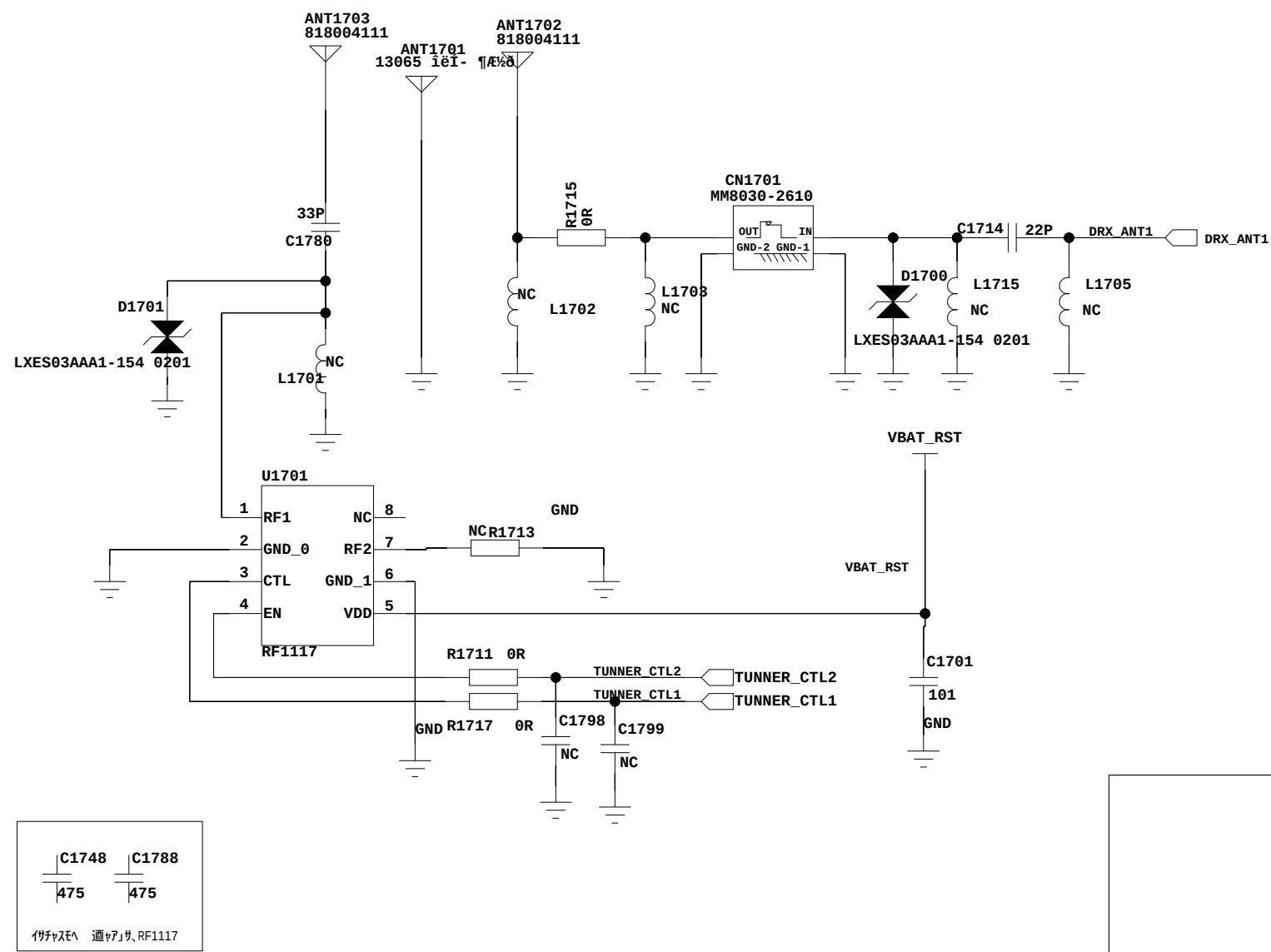


B40 TX

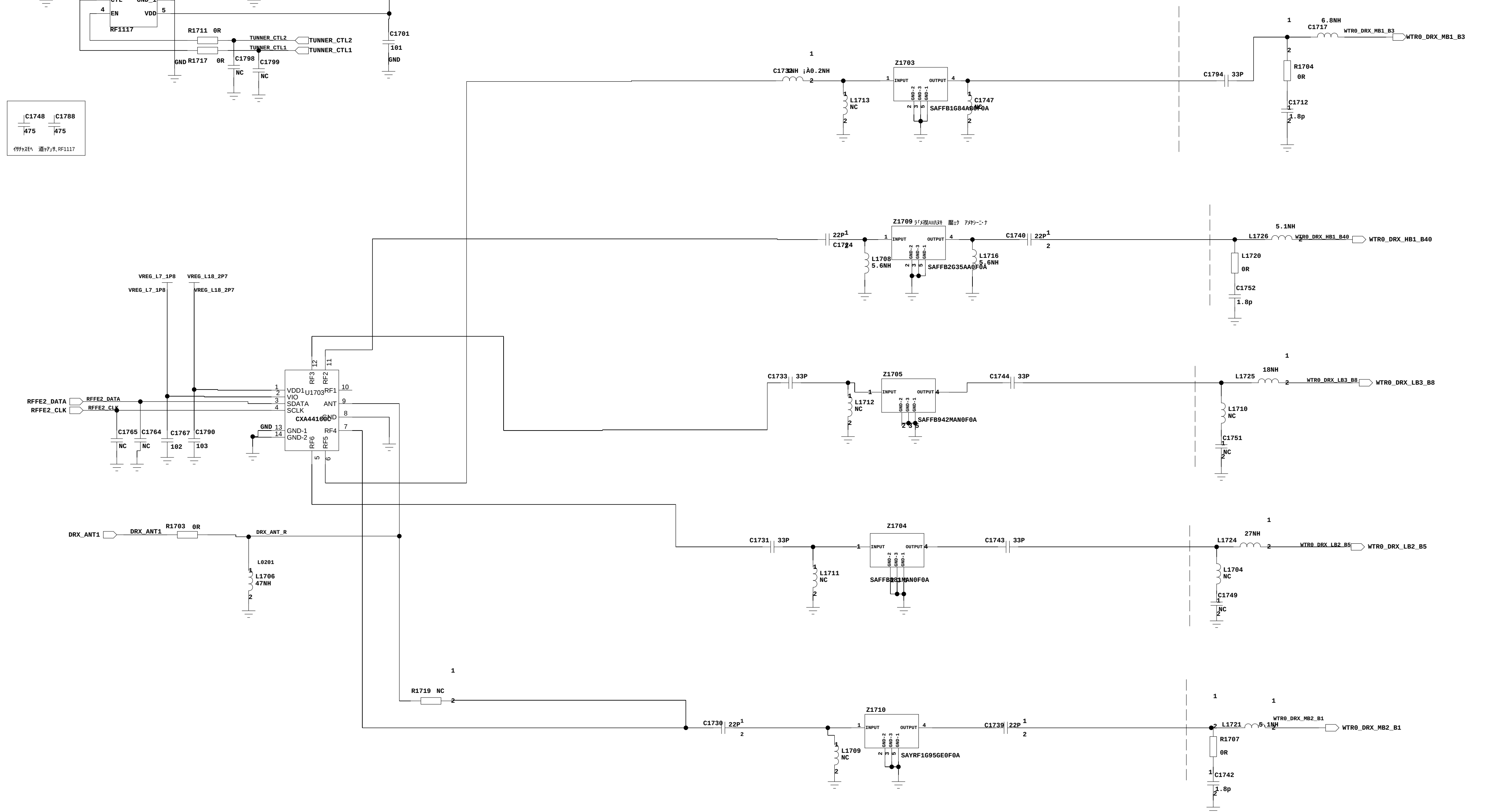


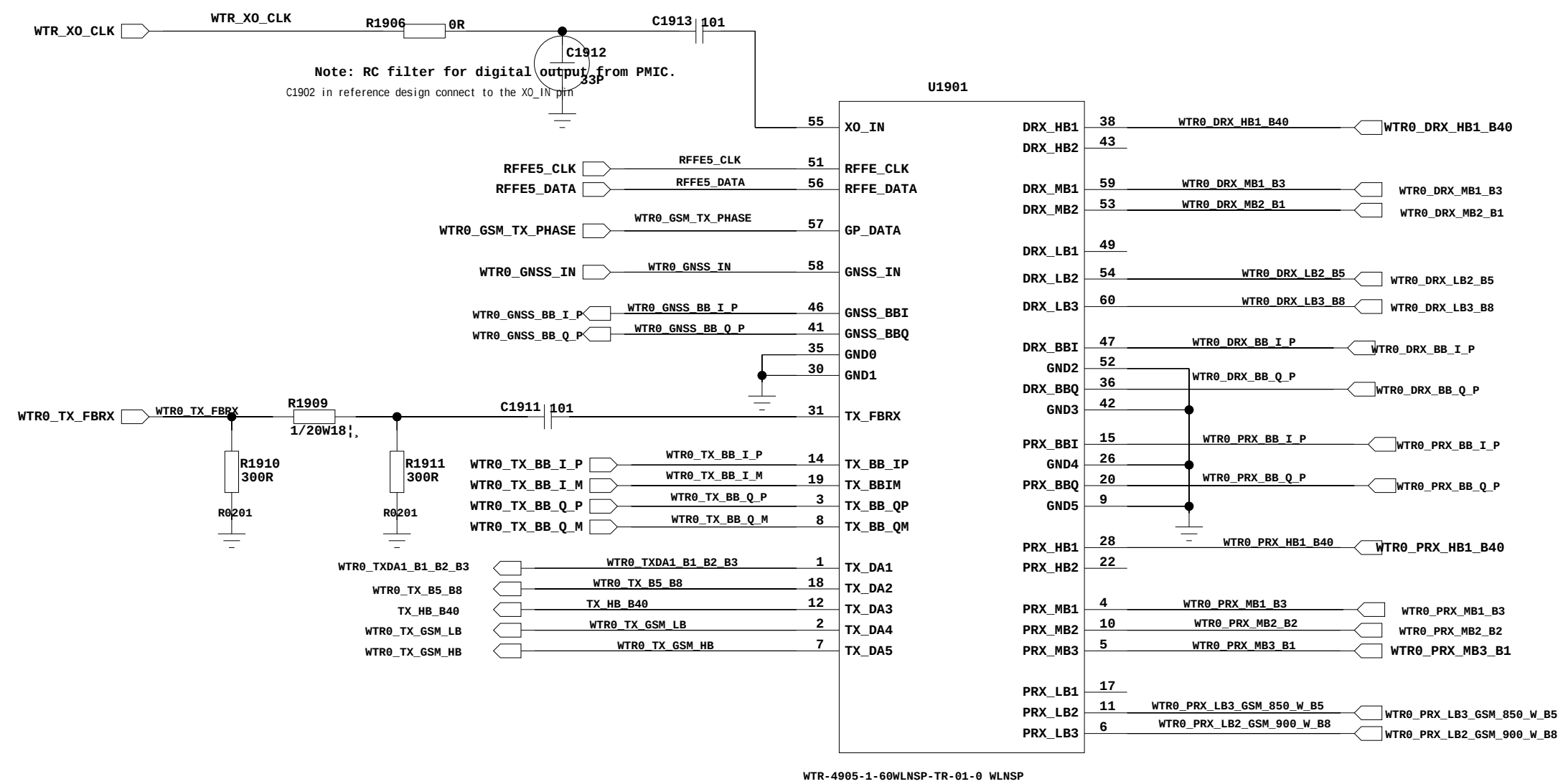
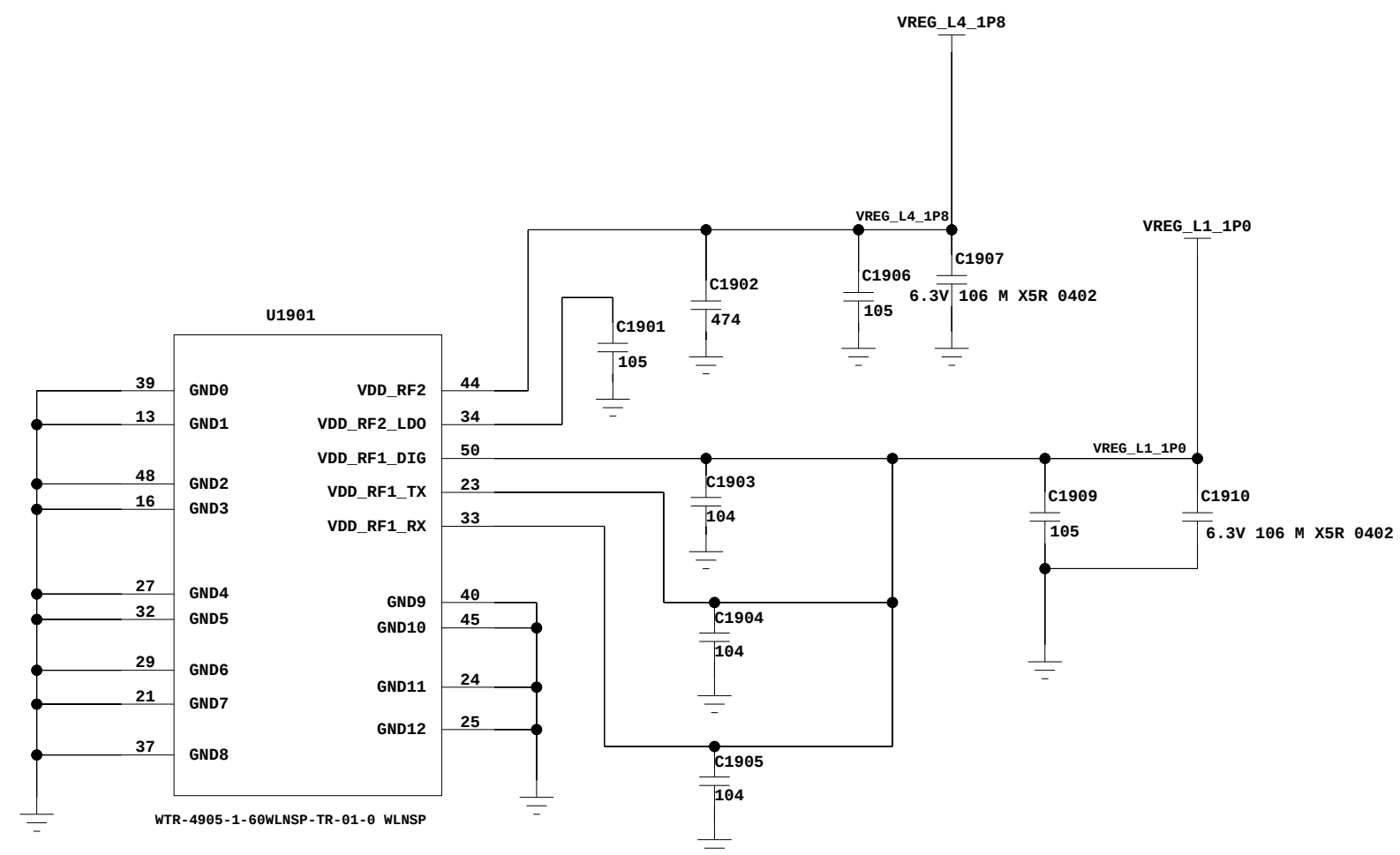
	Sign	Date	Type:
Drawn By			Draw No: Schematic1
Checked By			Sheet 30 of 28 Ver:
Standard By			oppo
Approved By			

LTE DRX



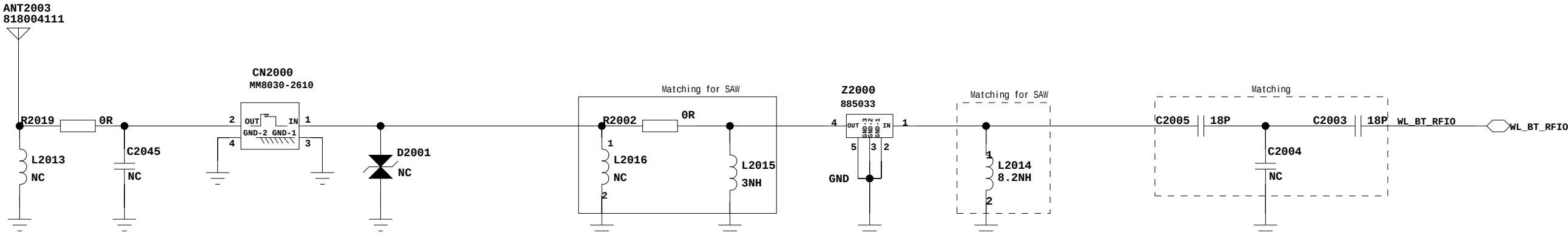
Place LNA matching components close to WTR4905



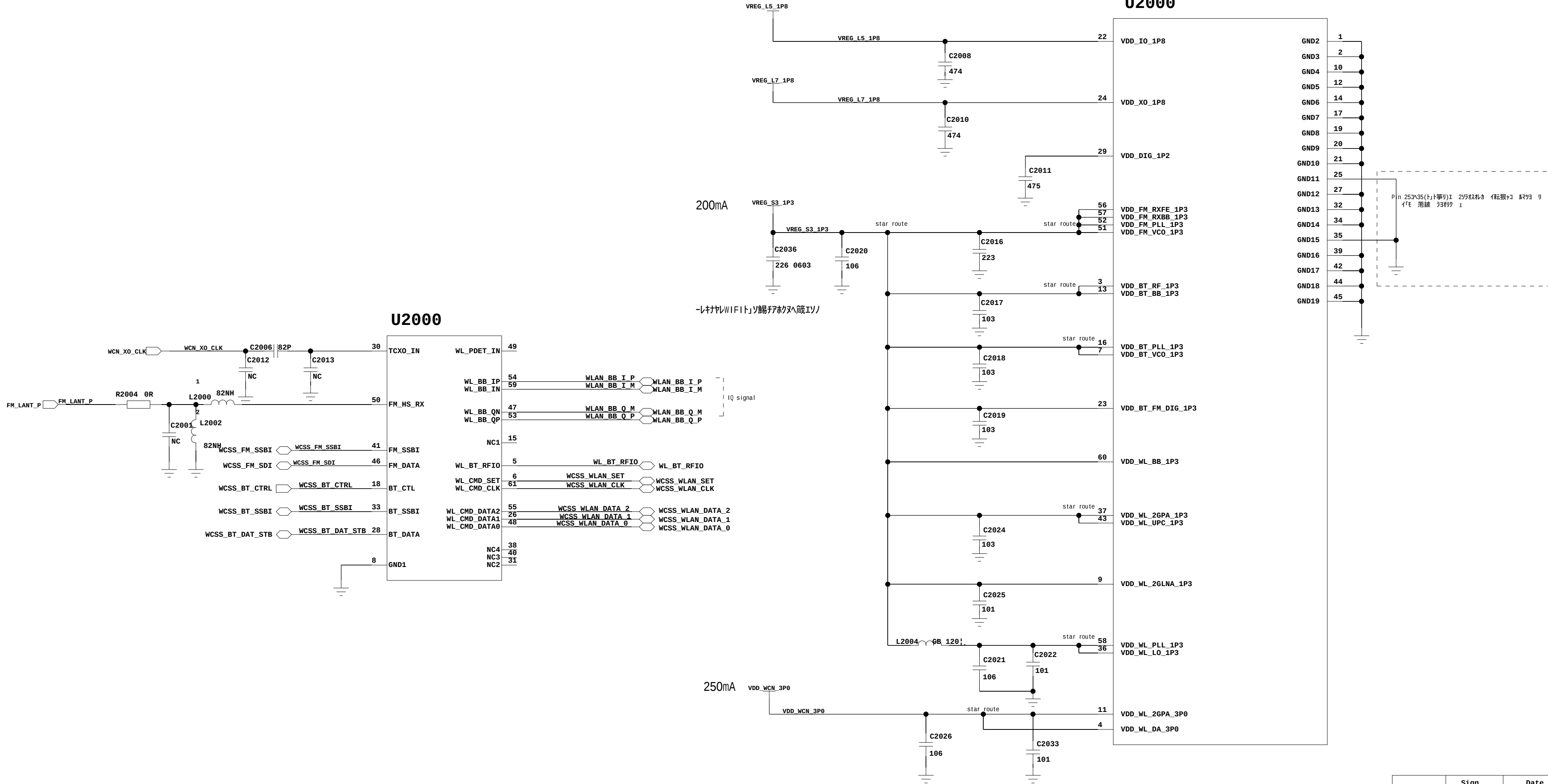


	Sign	Date	Type:	
Drawn By			Draw No: Schematic1	
Checked By			Sheet 9 of 9	WTR16023RF Ver:
Standard By				
Approved By				

WIFI+BT

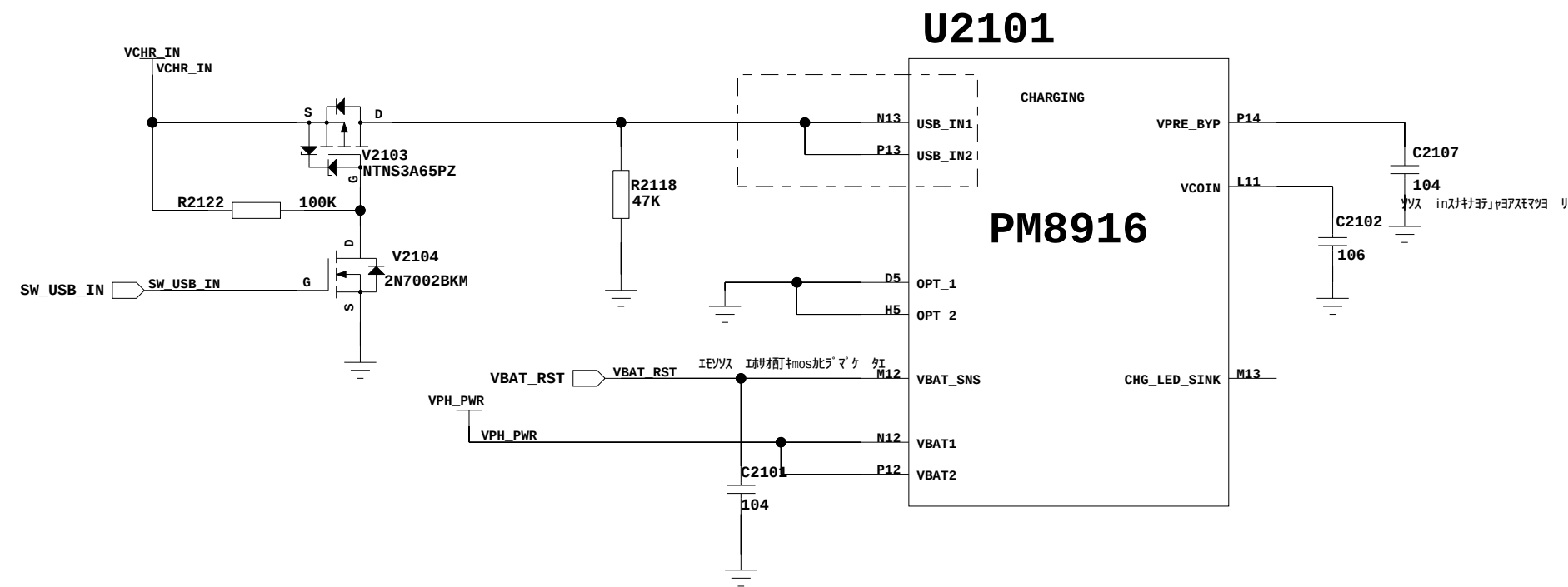


I 天线匹配网络

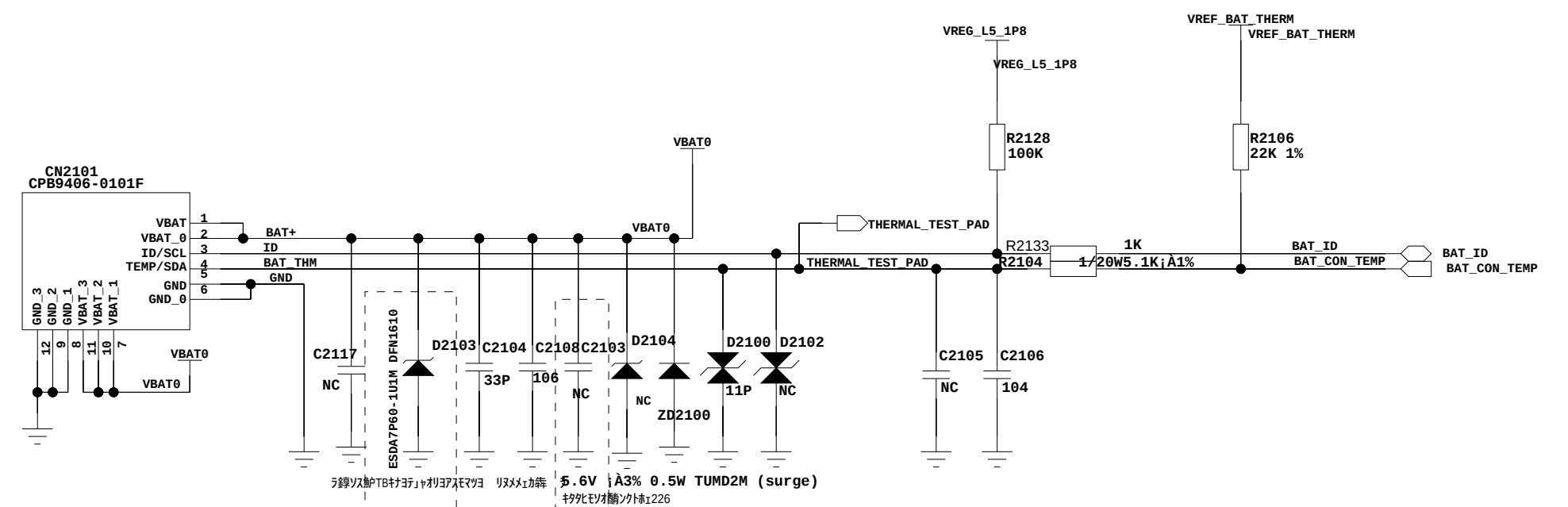
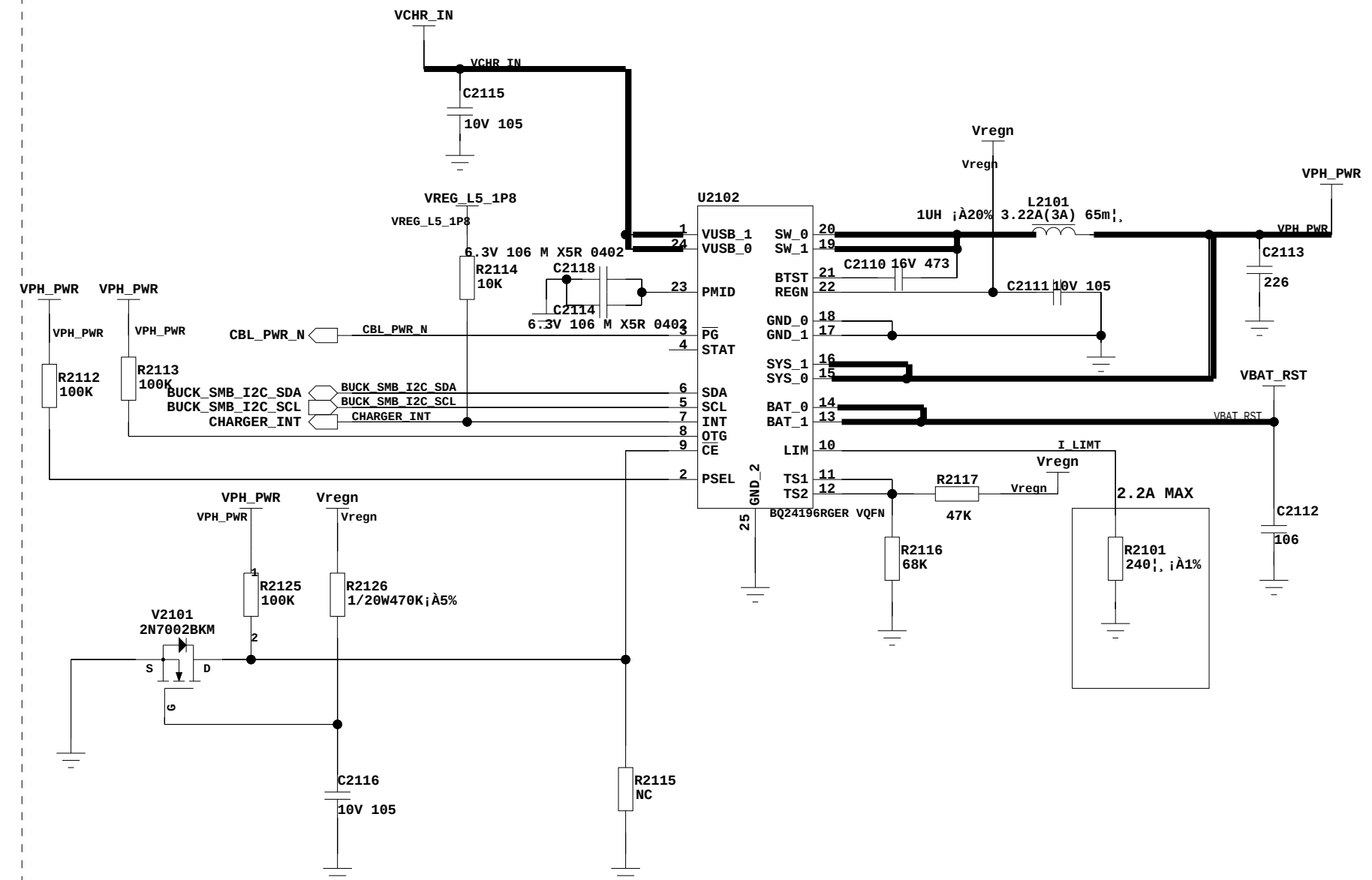


	Sign	Date	Type:
Drawn By			Draw No: 15189
Checked By			Sheet of 10 Ver:
Standard By			oppo
Approved By			

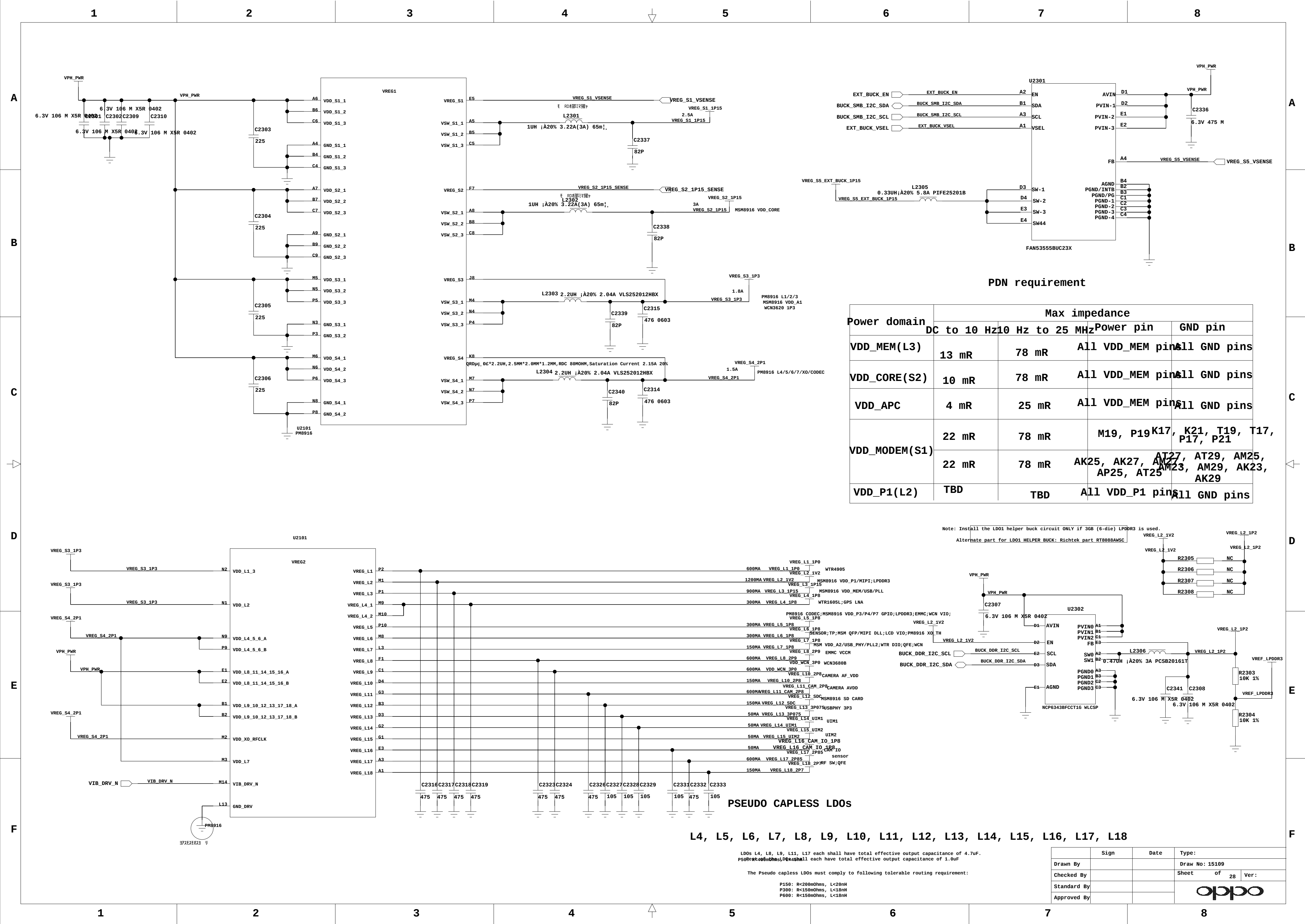
Pins	Hi Z	GND
OPT_1	No SMB1360	SMB1360 present
OPT_2	No Ext APC buck (\$5)	APC buck (\$5) present



Circuit of Ext. Charge



	Sign	Date	Type:		
Drawn By			Draw No: 15109		
Checked By			Sheet	of 11	Ver:
Standard By					
Approved By					

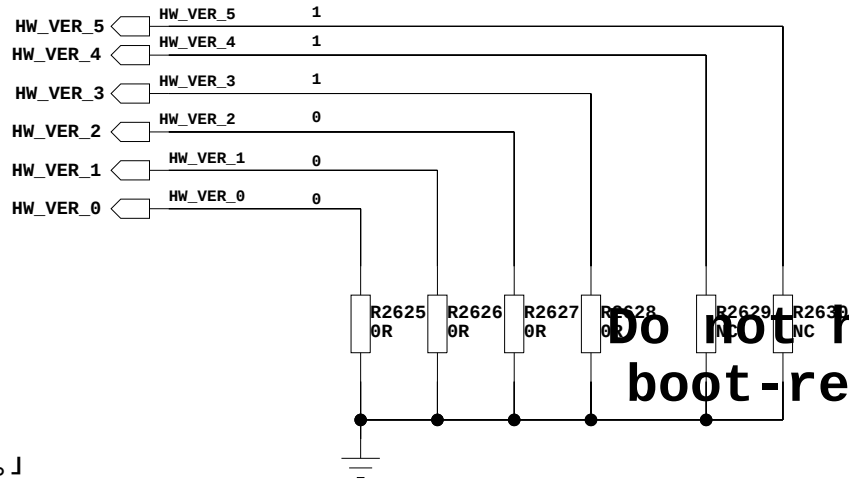


A

モイ - 豎奴 ヨコ

	GPIO_11	GPIO_10	GPIO_52
	HW_VER_5	HW_VER_4	HW_VER_3
△377	1	1	0
モ、跡-	0	1	1
フハ観	1	0	0
36-	0	0	1

	GPIO_51	GPIO_50	GPIO_49
0244722%	HW_VER_2	HW_VER_1	HW_VER_0
Ver1.0	0	0	0
Ver1.1	0	0	1
Ver1.2	0	1	0
Ver1.3	1	0	0

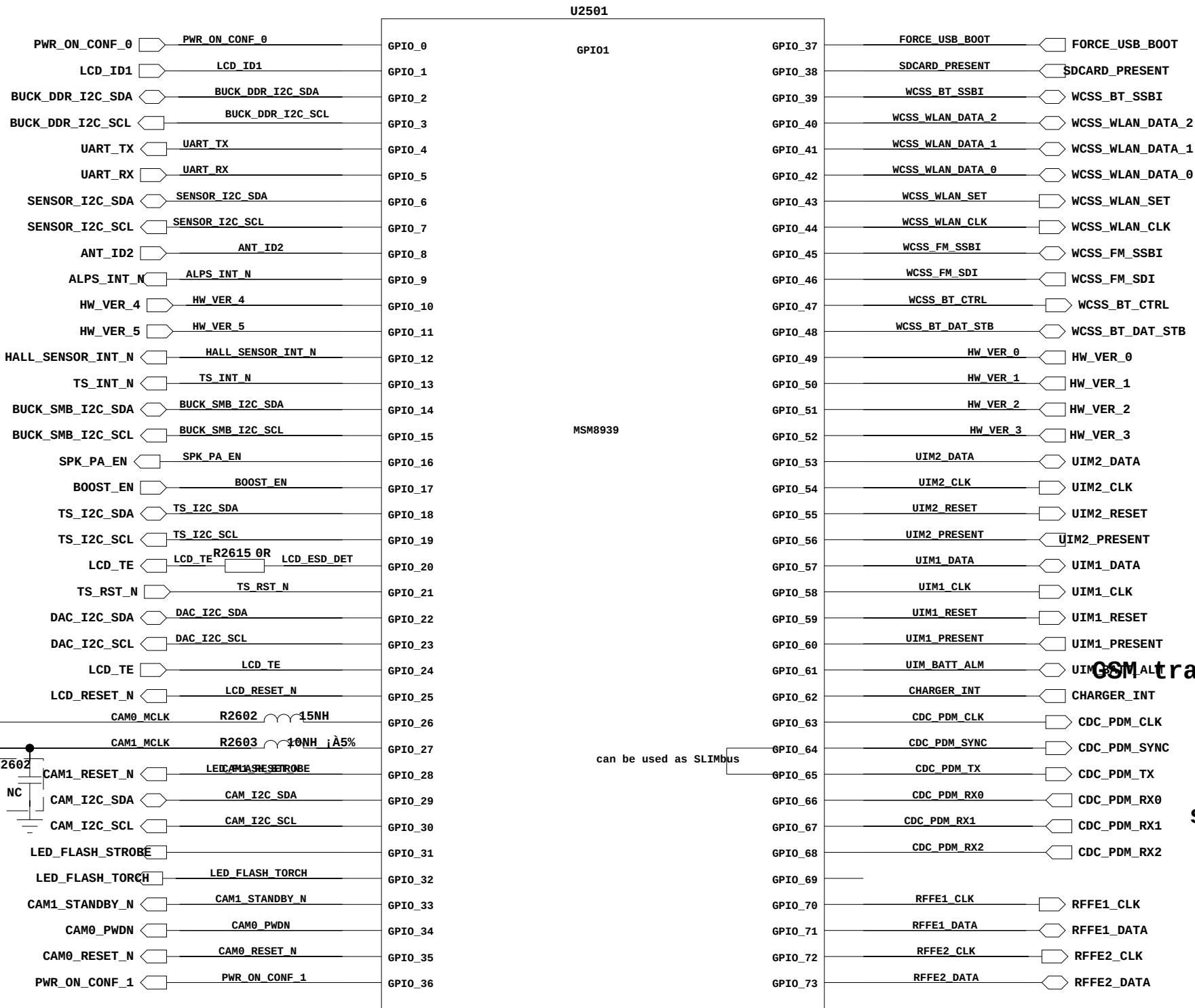


note:

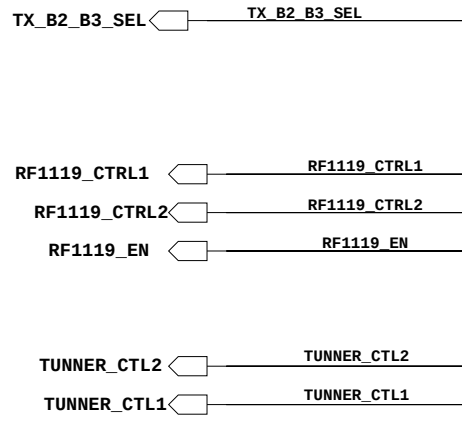
Do not have a pull-up on the following GPIOs, unless they are intended for boot or secure boot-related configurations GPIO 97, 94, 93, 92, 91, 89, 87,88, 86, 90, 81, 82, 83.

A

B

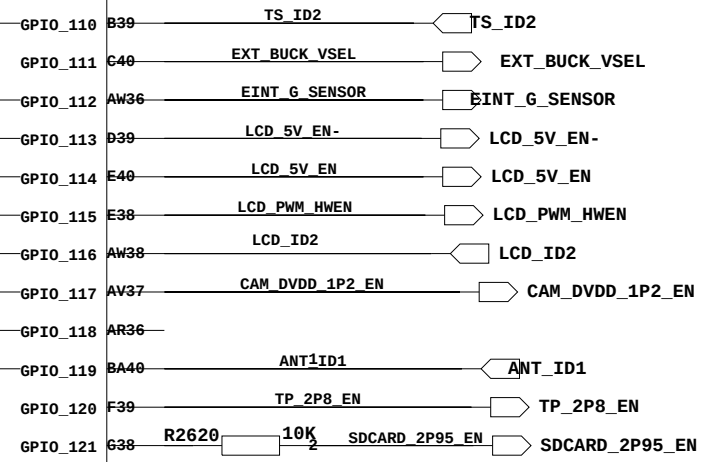


GRFC signals



I2S-1

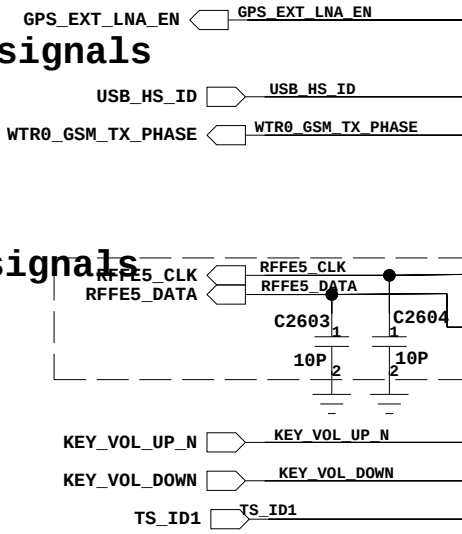
I2S-2



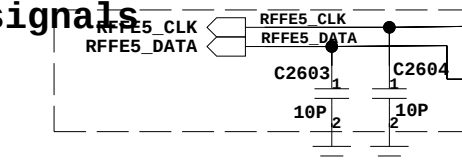
B

C

GSM transmit phase adjust signals

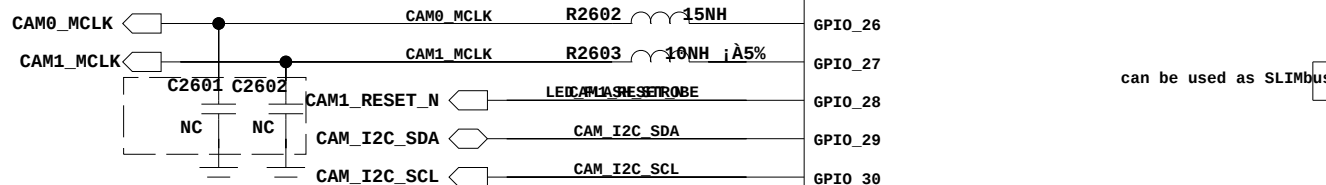


status and control signals

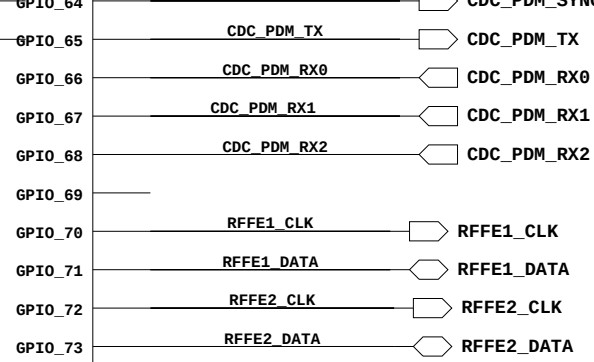


C

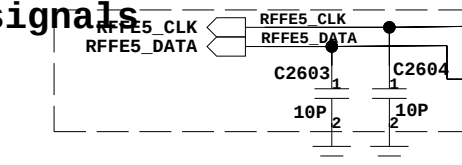
D



can be used as SLIMbus

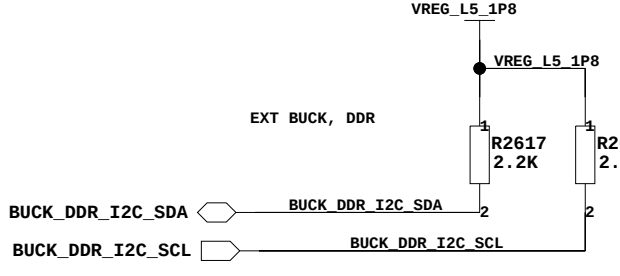
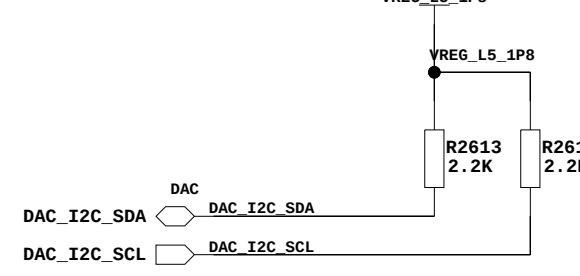
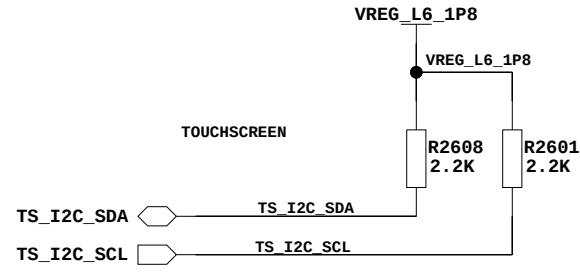
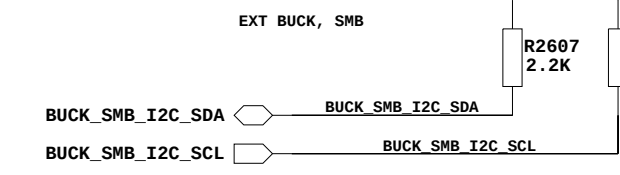
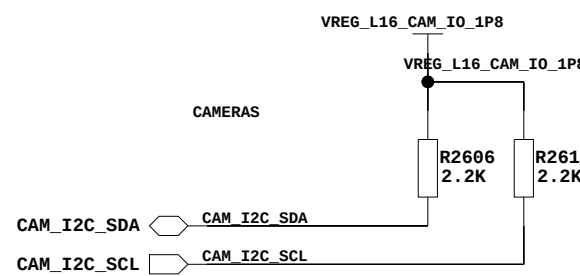
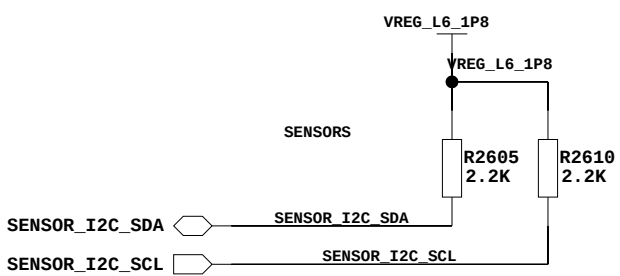


status and control signals



D

E



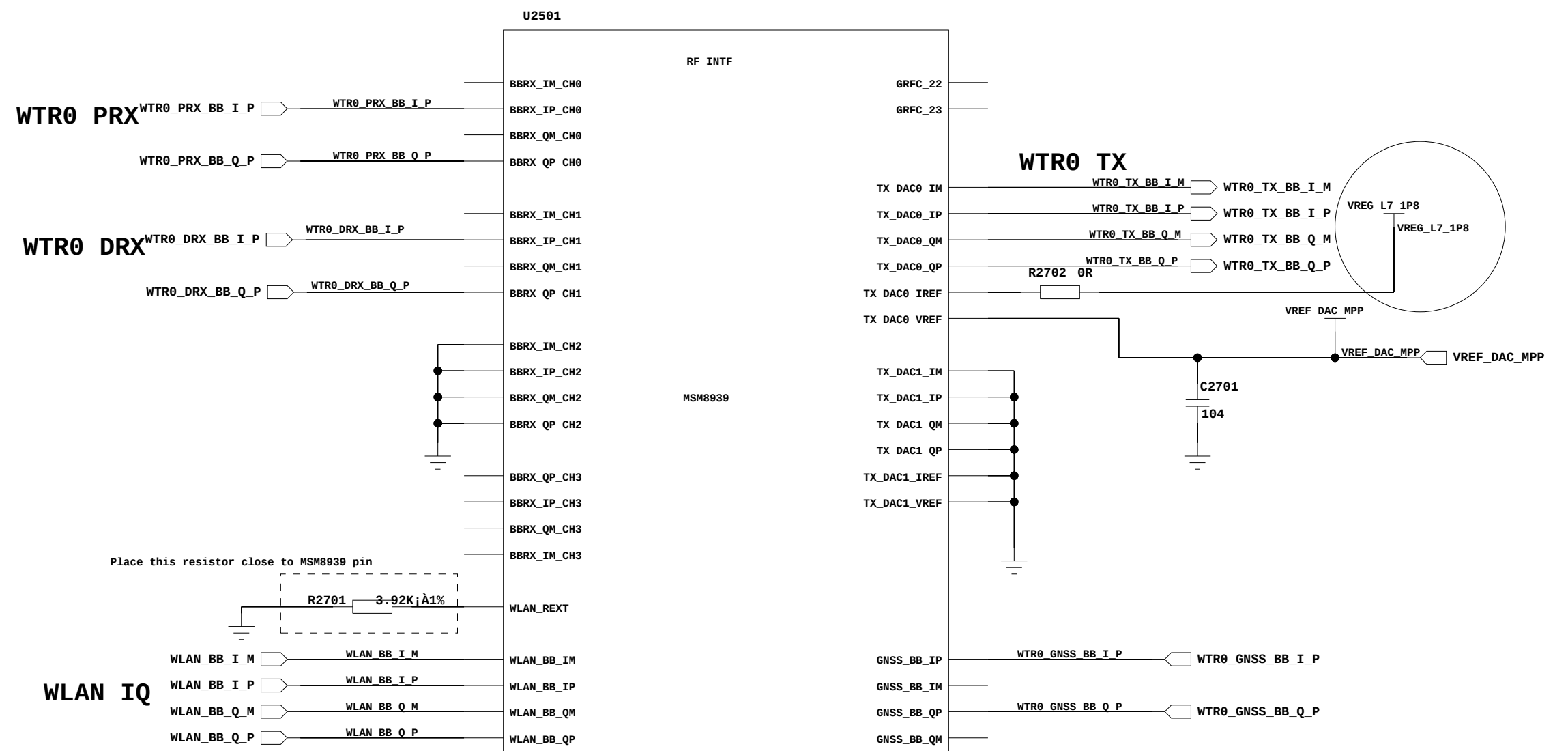
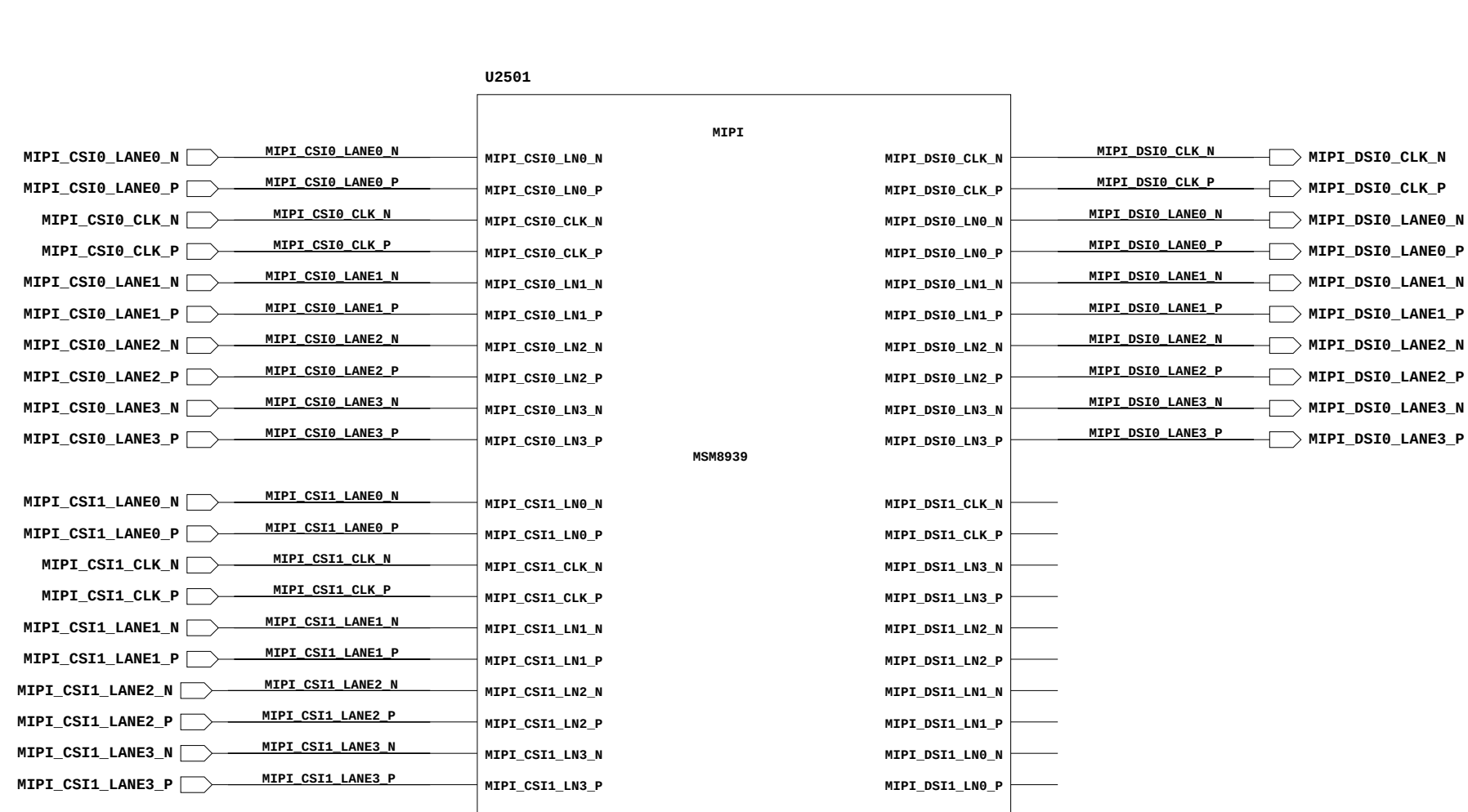
E

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	Sign	Date	Type:
Drawn By			Draw No: 15109
Checked By			Sheet of 16 Ver:
Standard By			
Approved By			

oppo

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	Sign	Date	Type:	
Drawn By			Draw No: 15109	
Checked By			Sheet	of 17 Ver:
Standard By				
Approved By				

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A

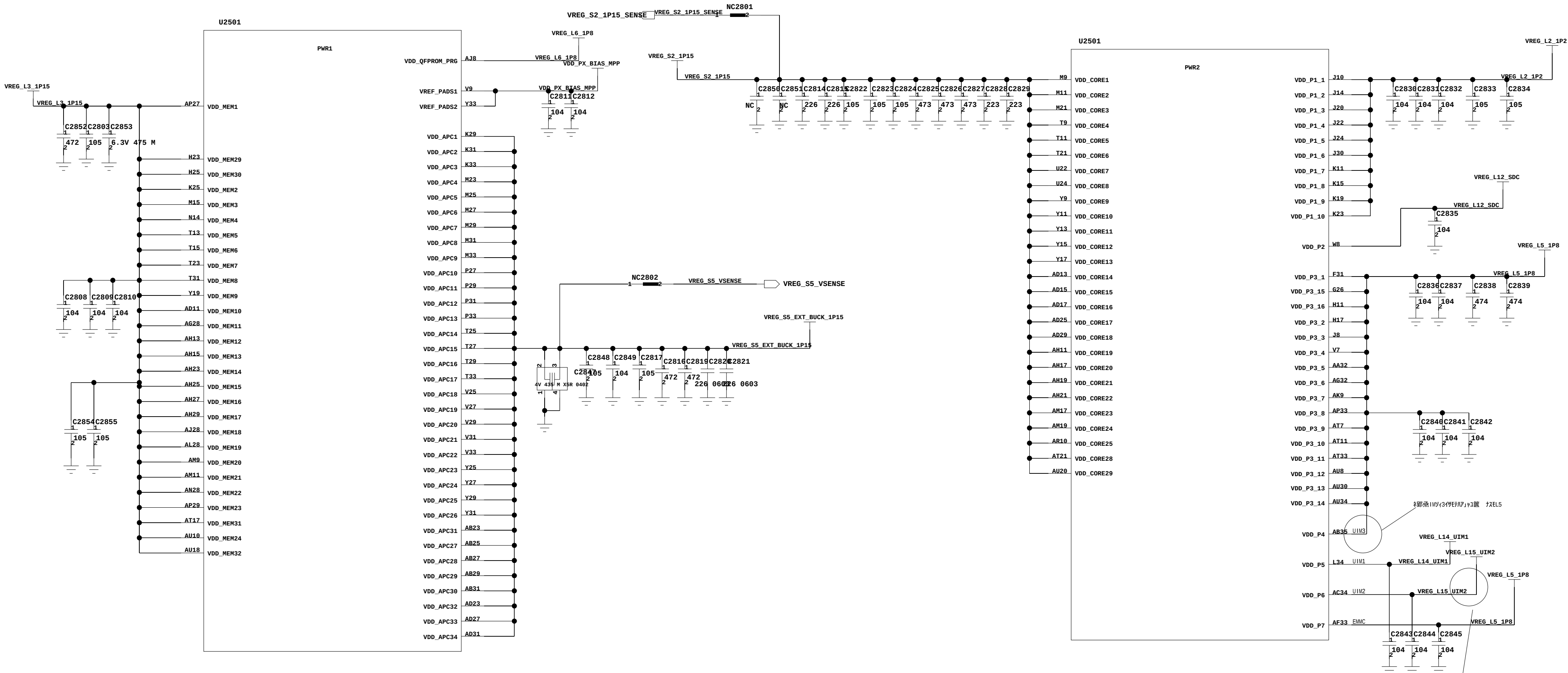
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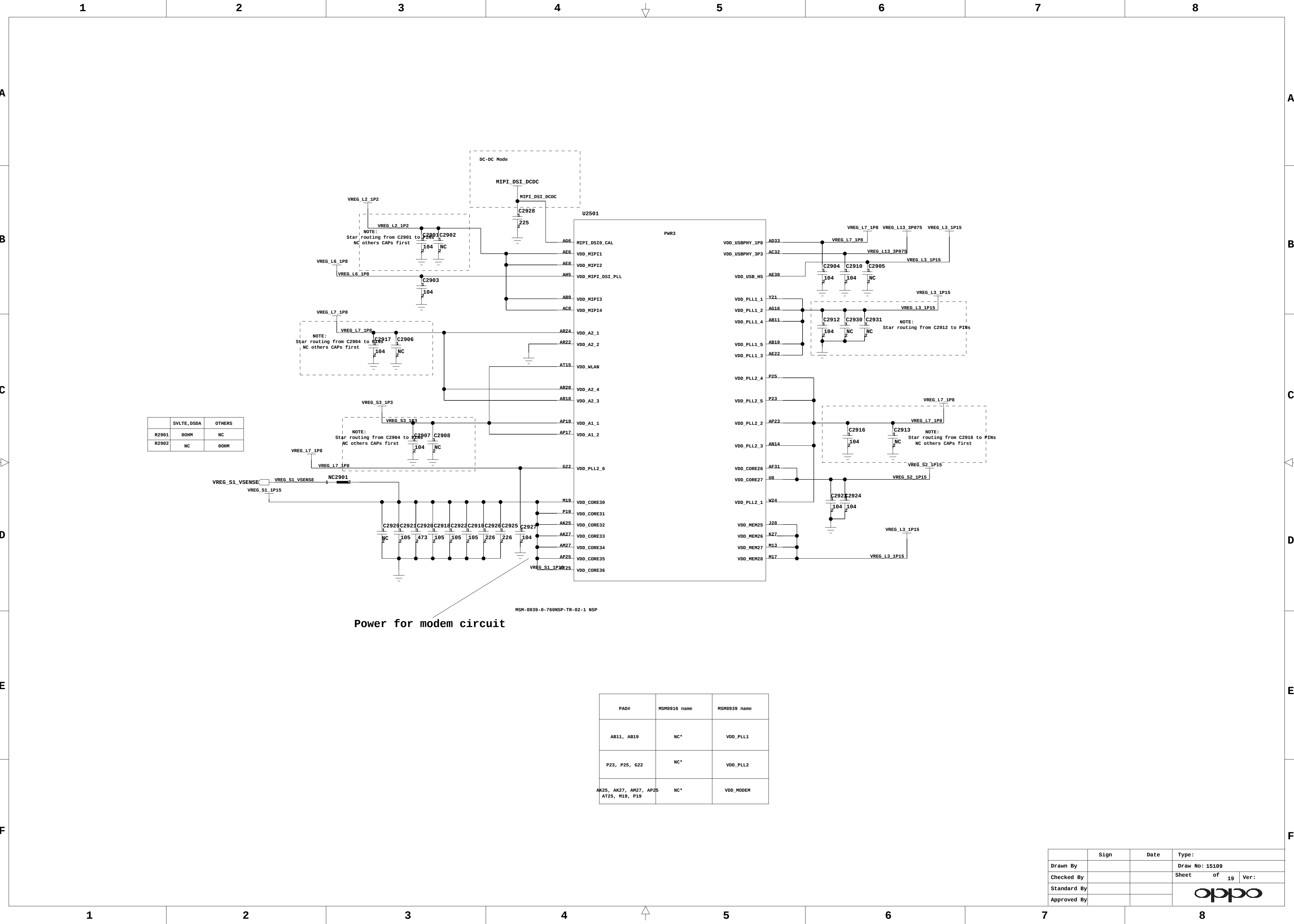


MSM-8939-0-760NSP-TR-02-1 NSP

MSM-8939-0-760NSP-TR-02-1 NSP

Regulator	MSM8916/36/39(UIM Only) TSTS without USB UICC Support	MSM8936/39(USB UICC Only) TSTS with USB UICC Support
L14	NO	YES
L15	YES	NO

	Sign	Date	Type:
Drawn By			Draw No: 15109
Checked By			Sheet of 18 Ver:
Standard By			oppo
Approved By			



	SVLTE, DSDA	OTHERS
R2901	00HM	NC
R2902	NC	00HM

PAD#	MSM8916 name	MSM8939 name
AB11, AB19	NC*	VDD_PLL1
P23, P25, G22	NC*	VDD_PLL2
AK25, AK27, AM27, AP25, AT25, M19, P19	NC*	VDD_MODEM

	Sign	Date	Type:
Drawn By			Draw No: 15109
Checked By			Sheet of 19 Ver:
Standard By			oppo
Approved By			

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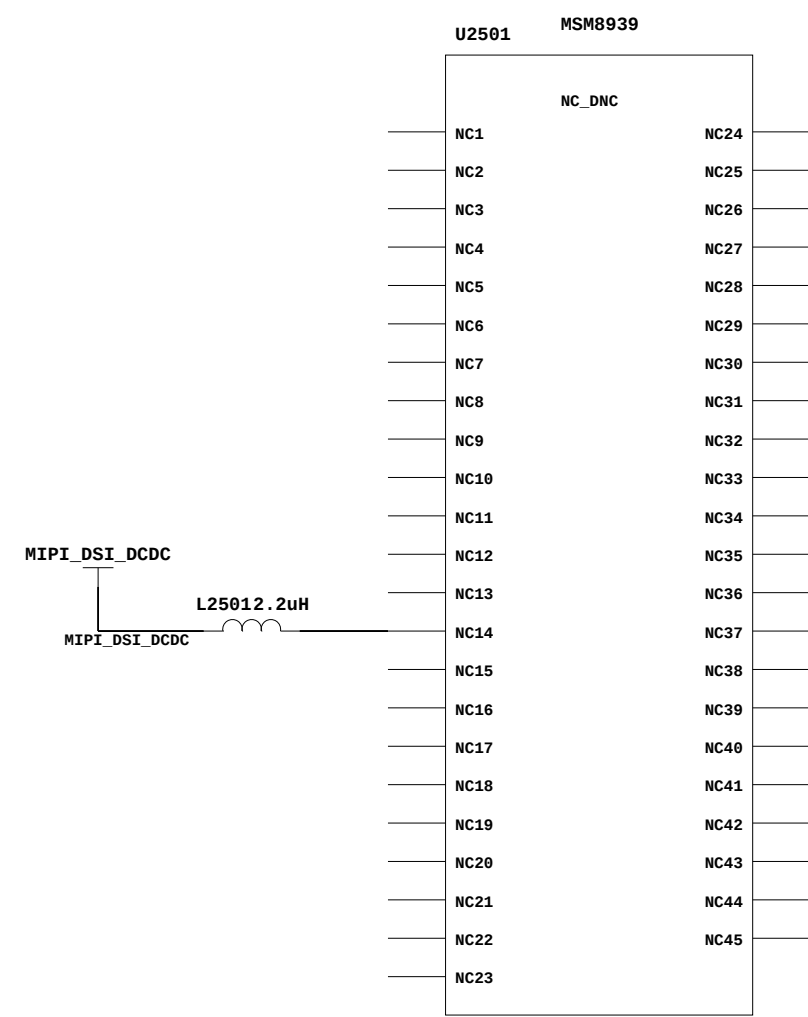
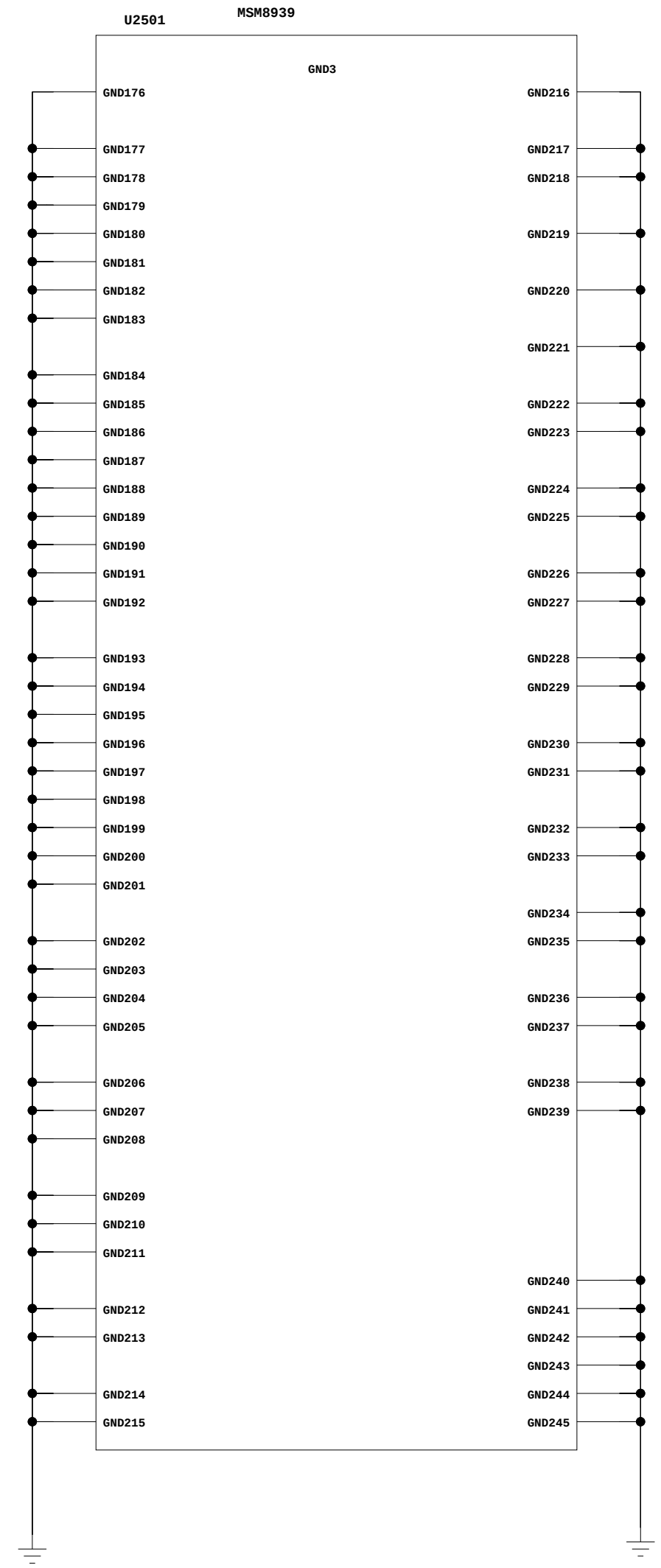
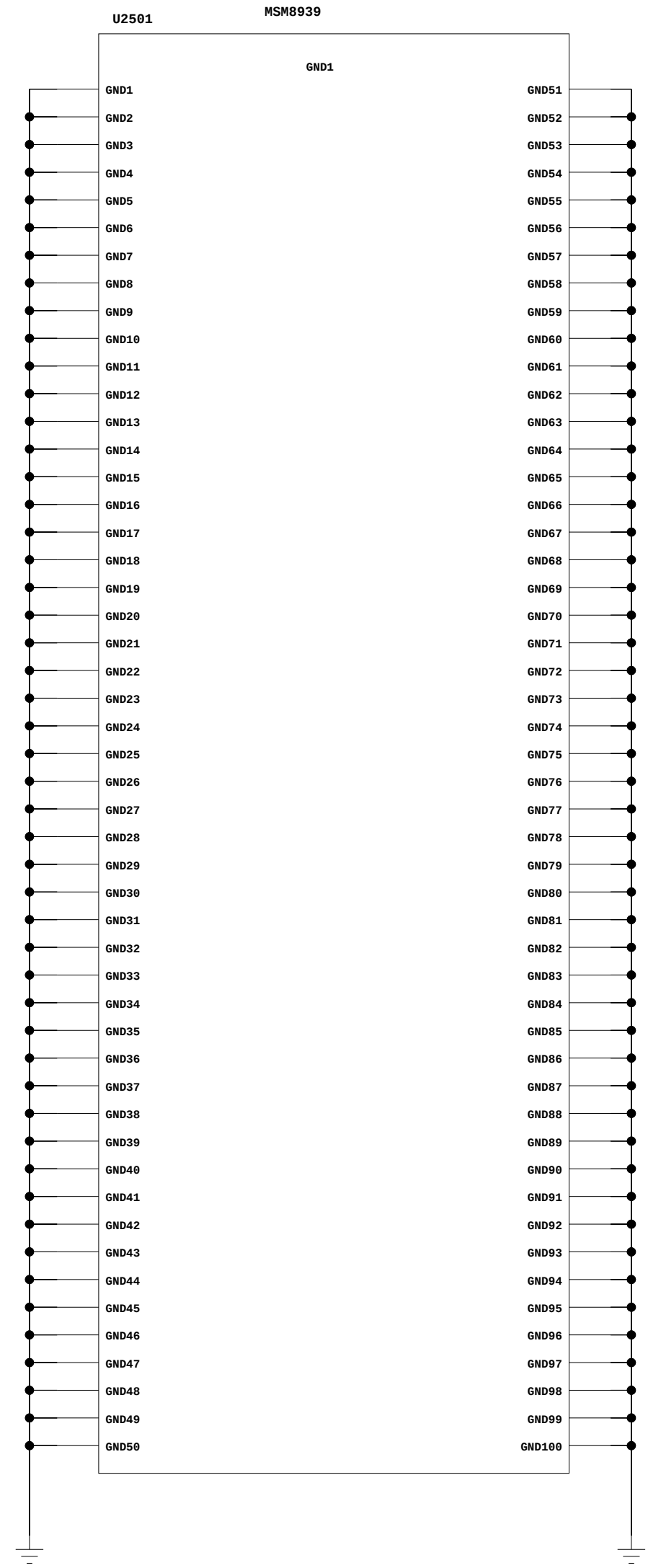
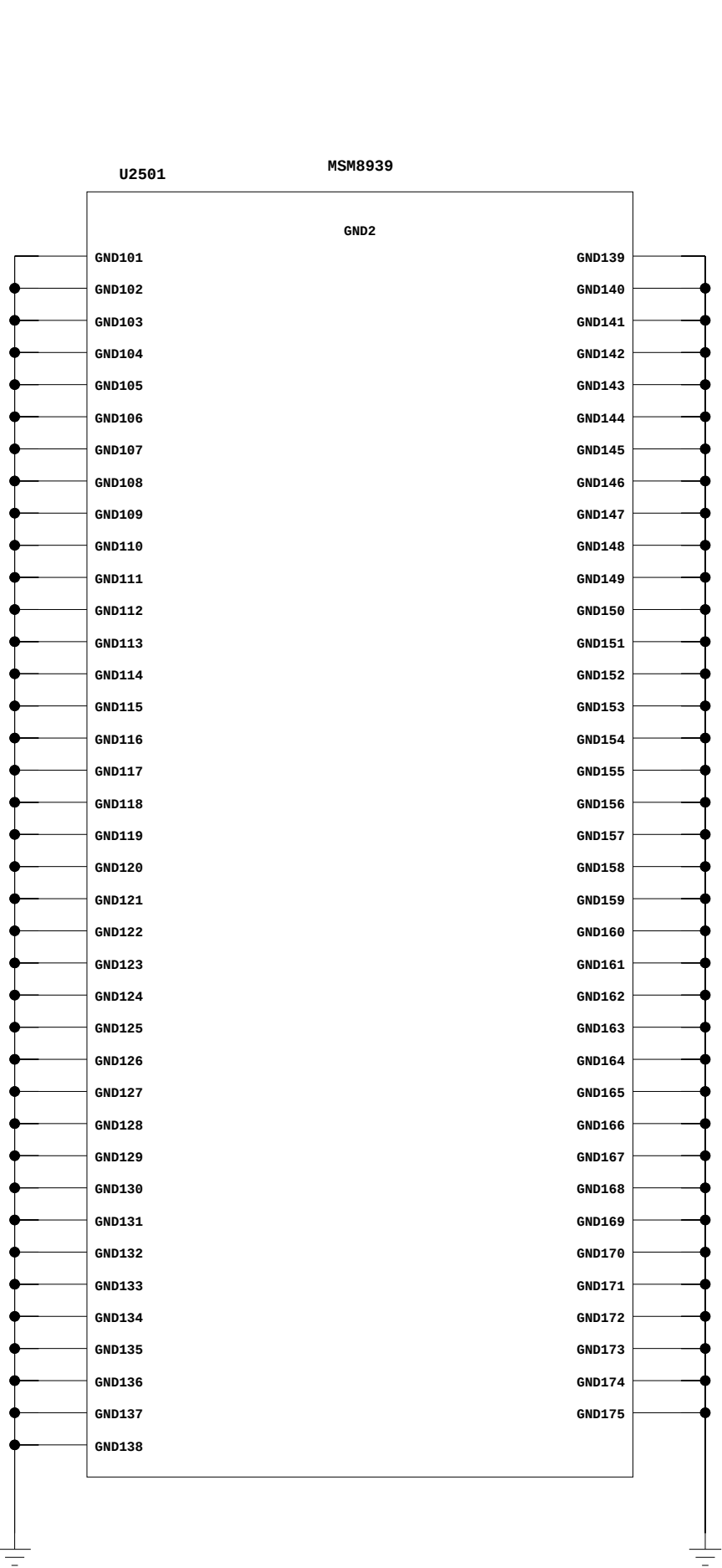
B

C

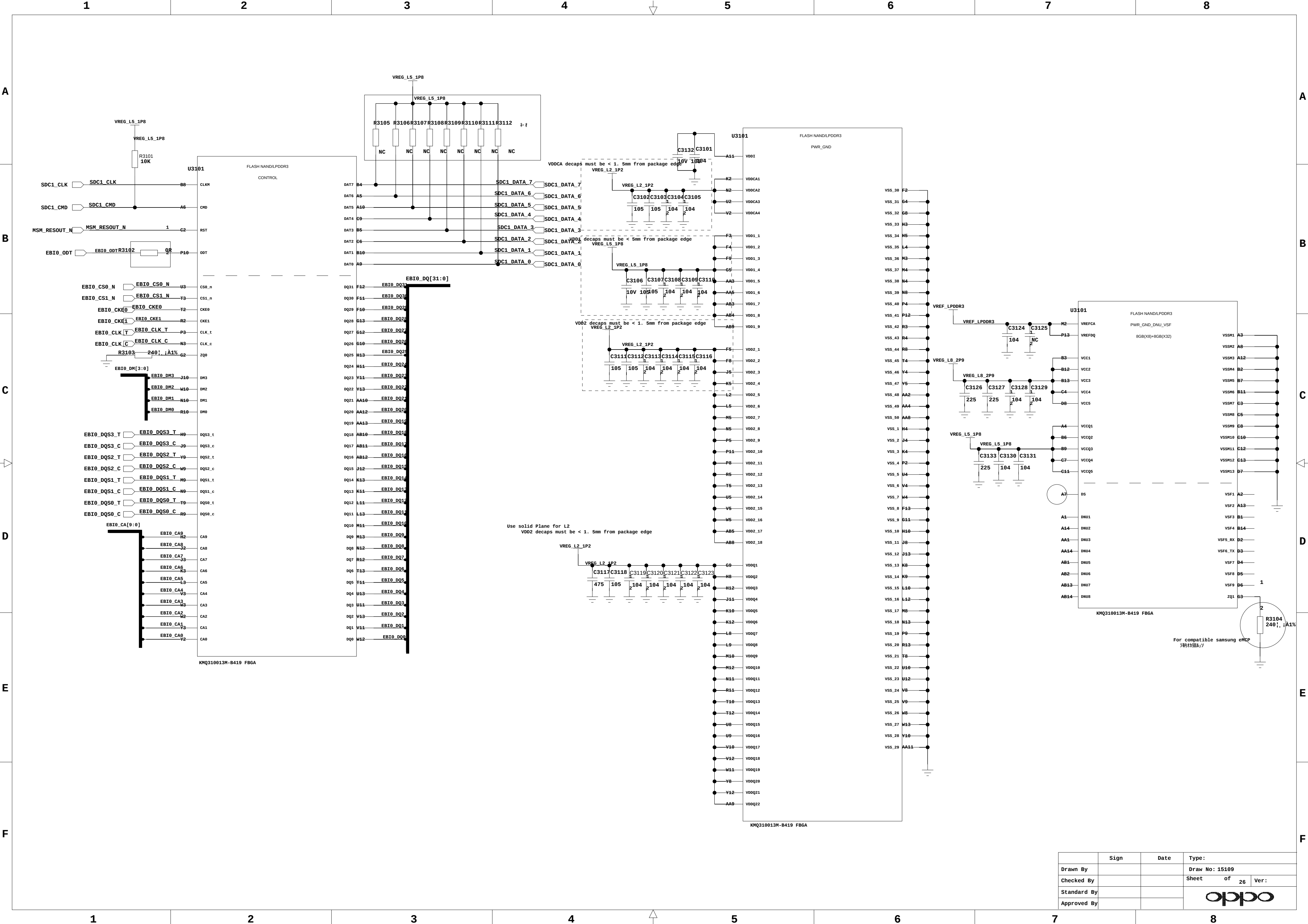
D

E

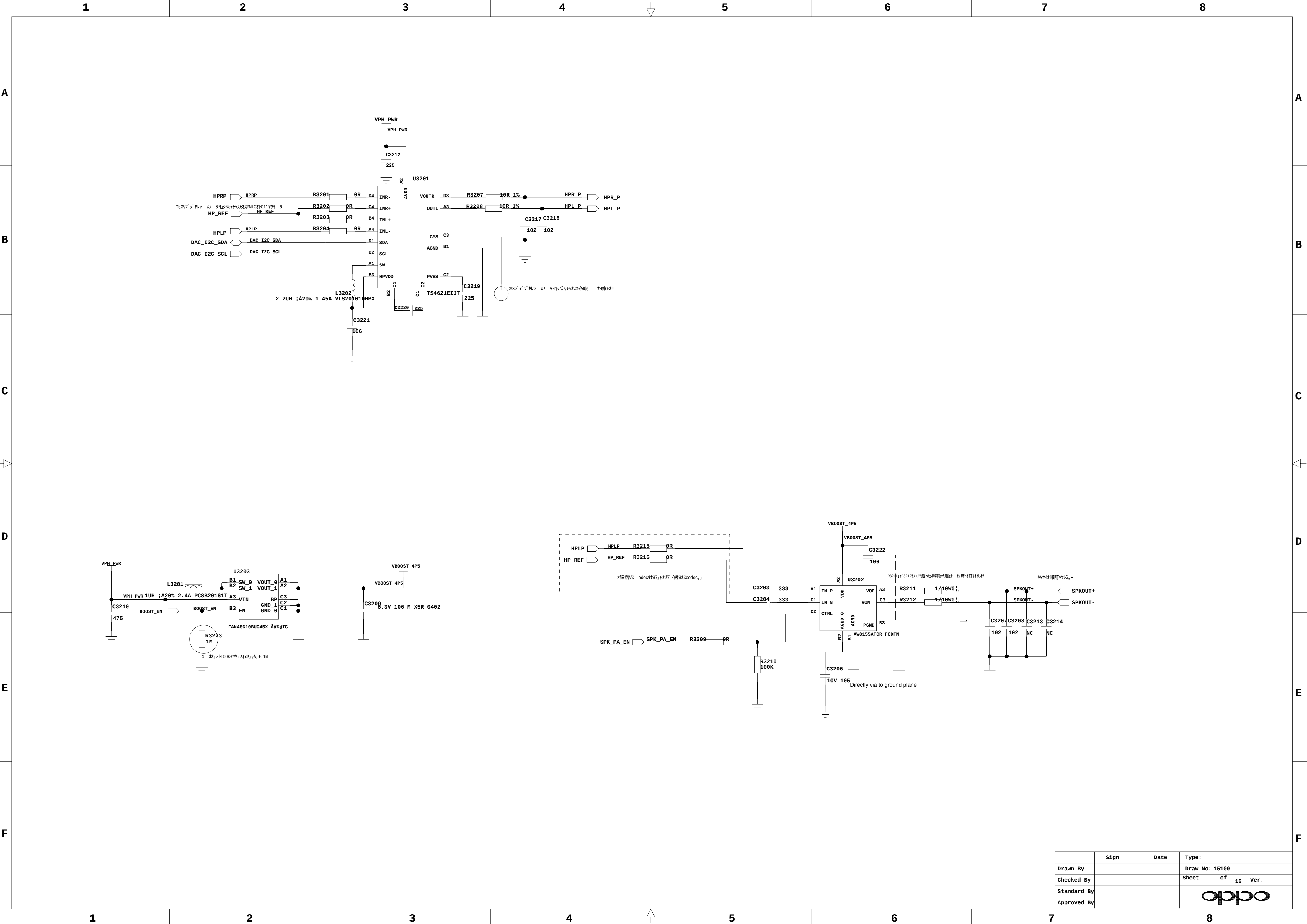
F



	Sign	Date	Type:		
Drawn By			Draw No: 15109		
Checked By			Sheet	of	25 Ver:
Standard By			oppo		
Approved By					

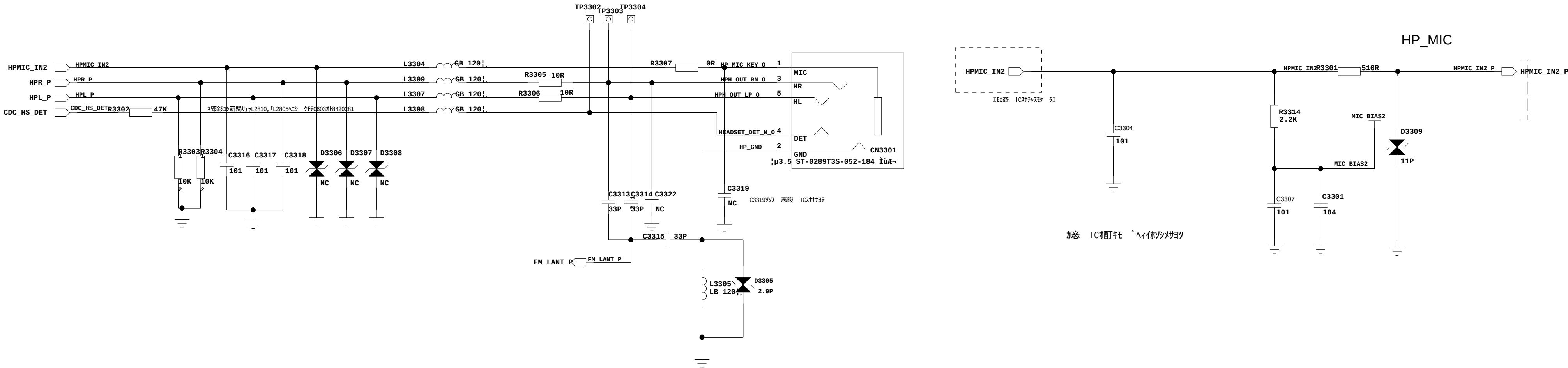


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Drawn By			Draw No: 15109
Checked By			Sheet of 26 Ver:
Standard By			oppo
Approved By			

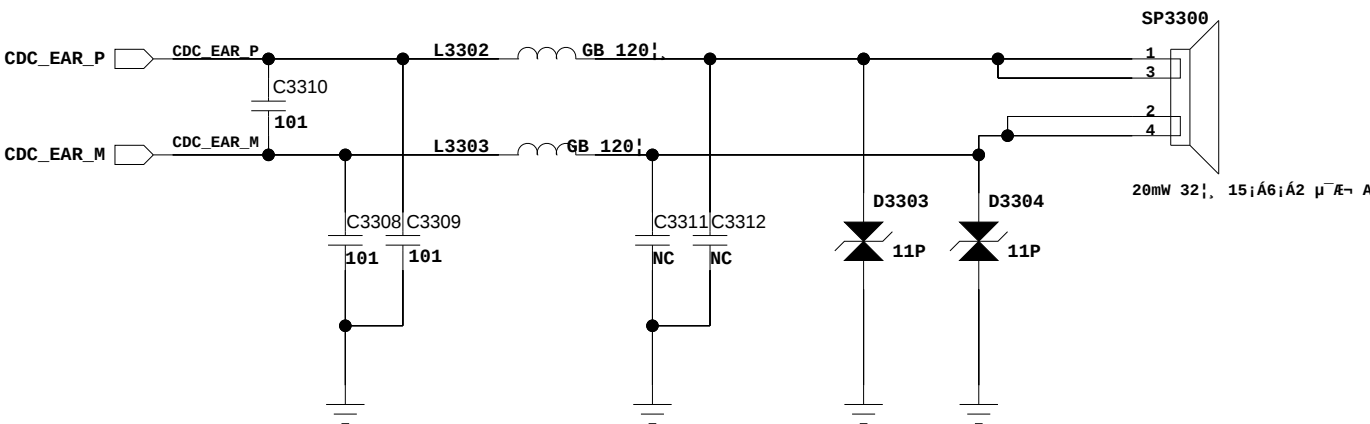


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Drawn By			Draw No: 15109
Checked By			Sheet of 15 Ver:
Standard By			oppo
Approved By			

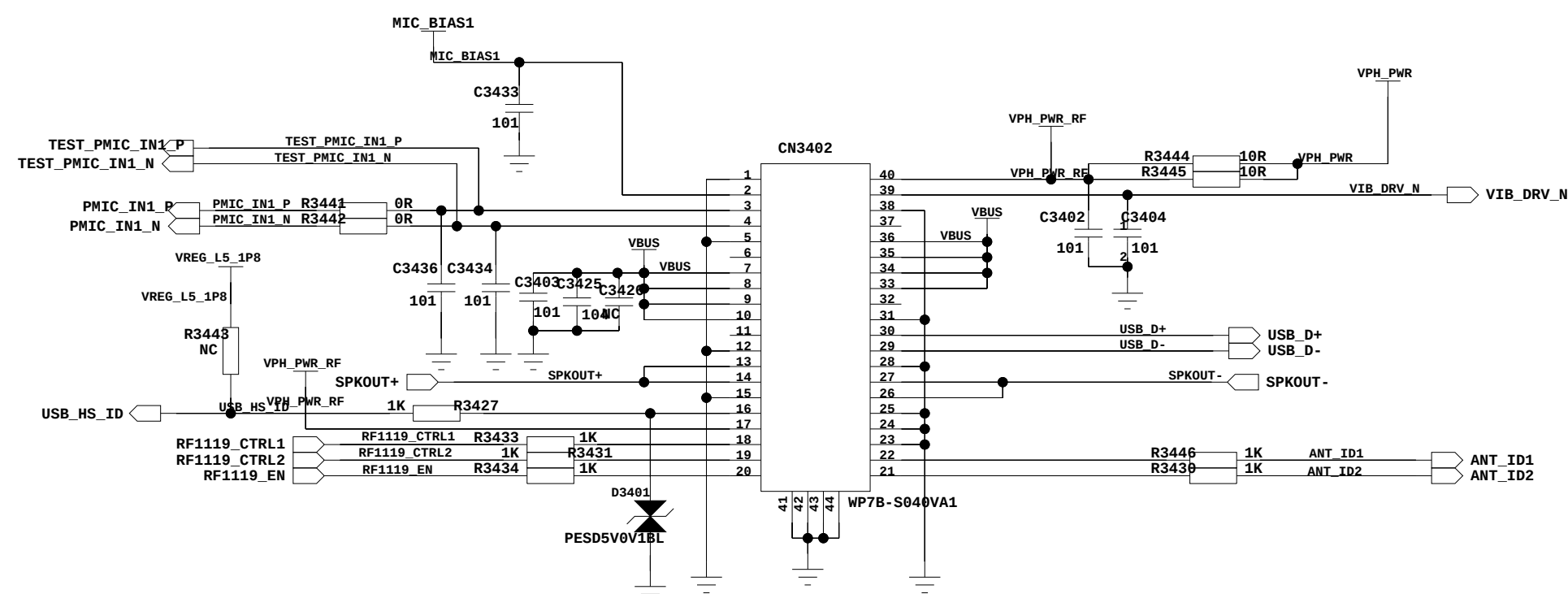
HP MIC



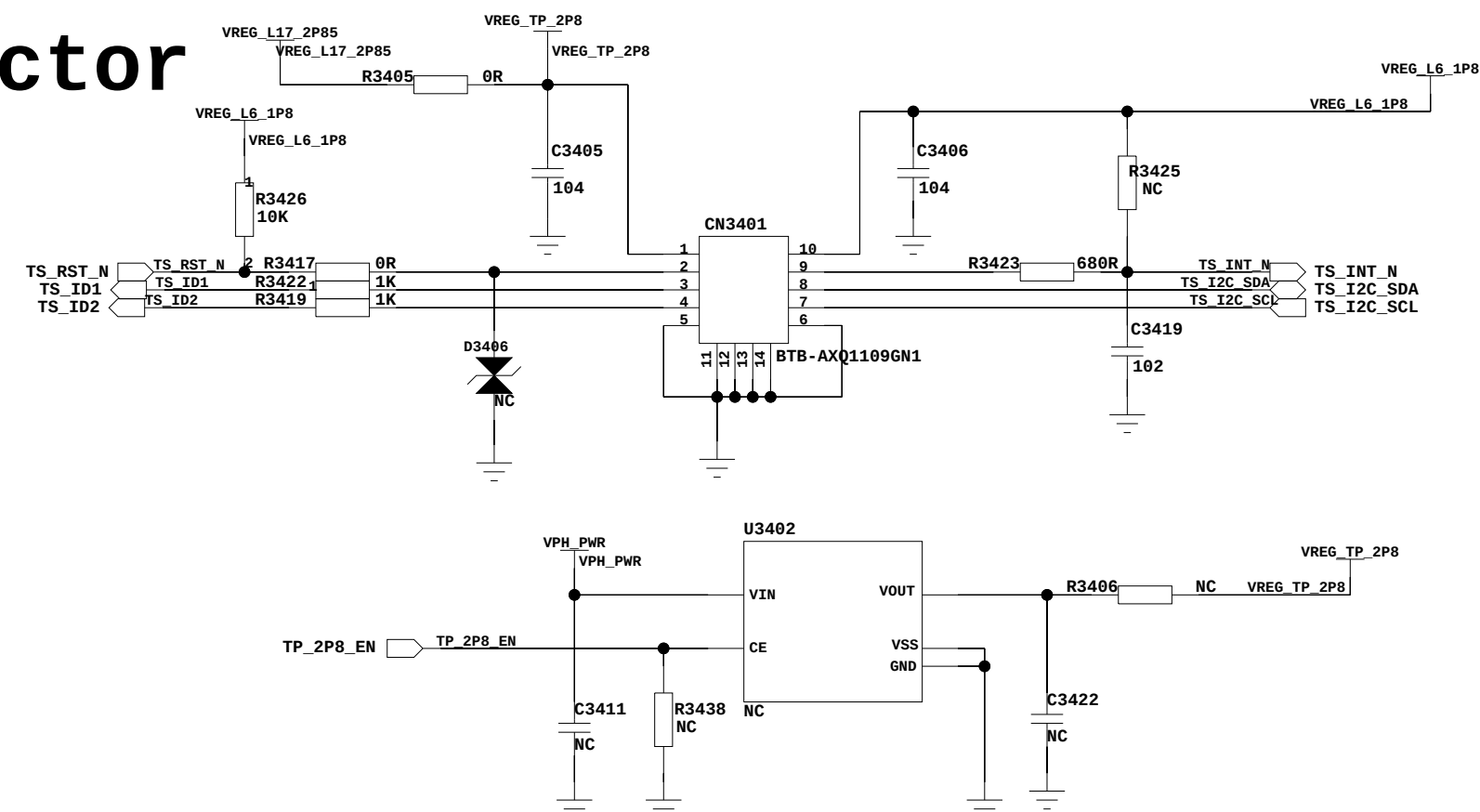
Receiver



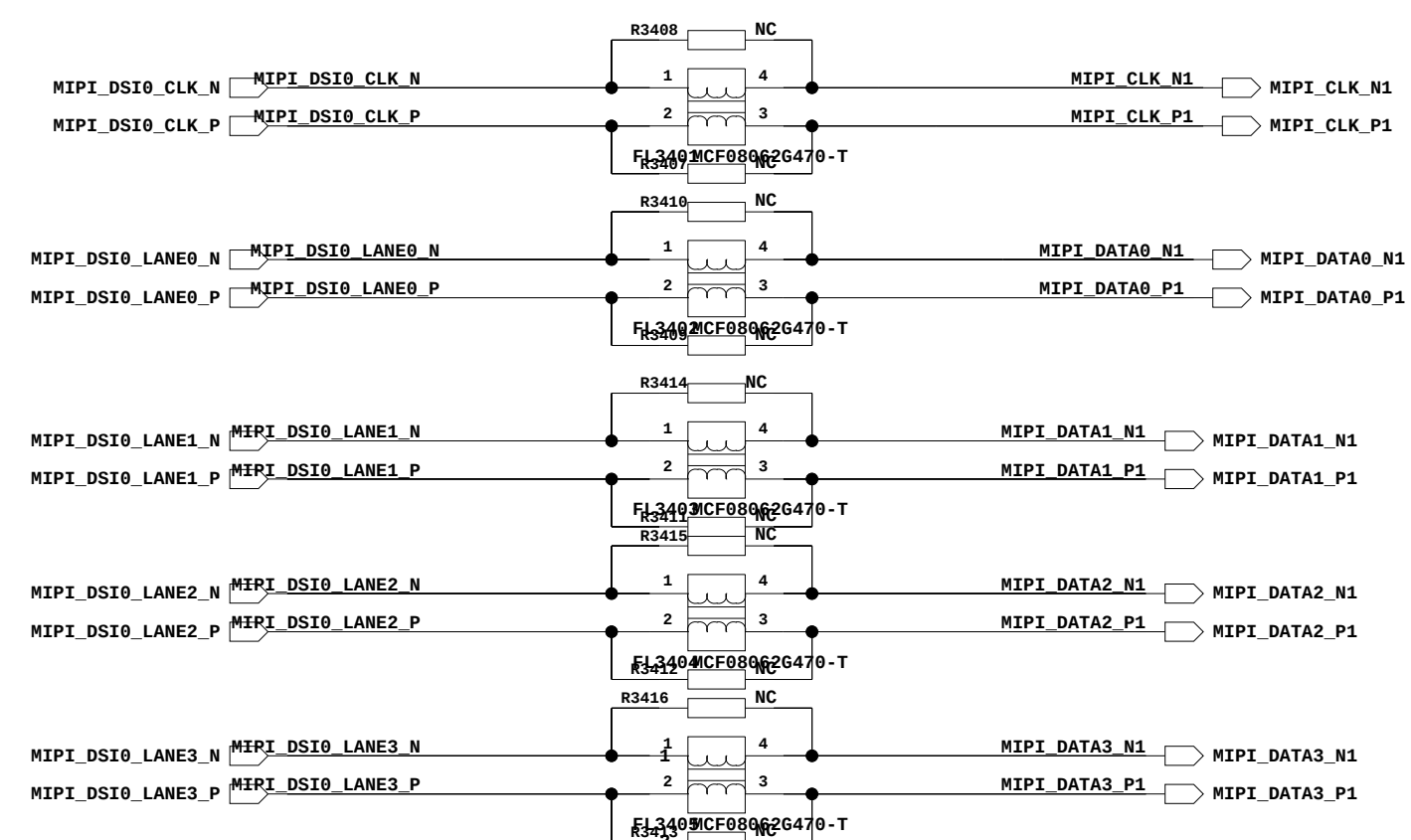
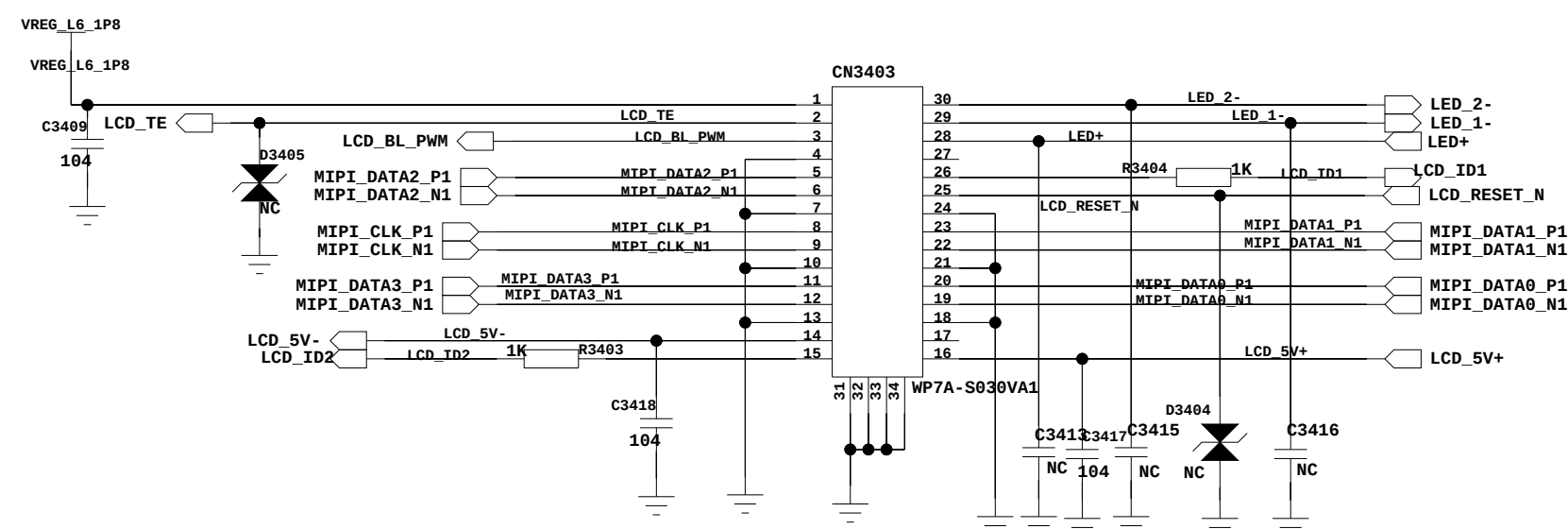
Sub board connector



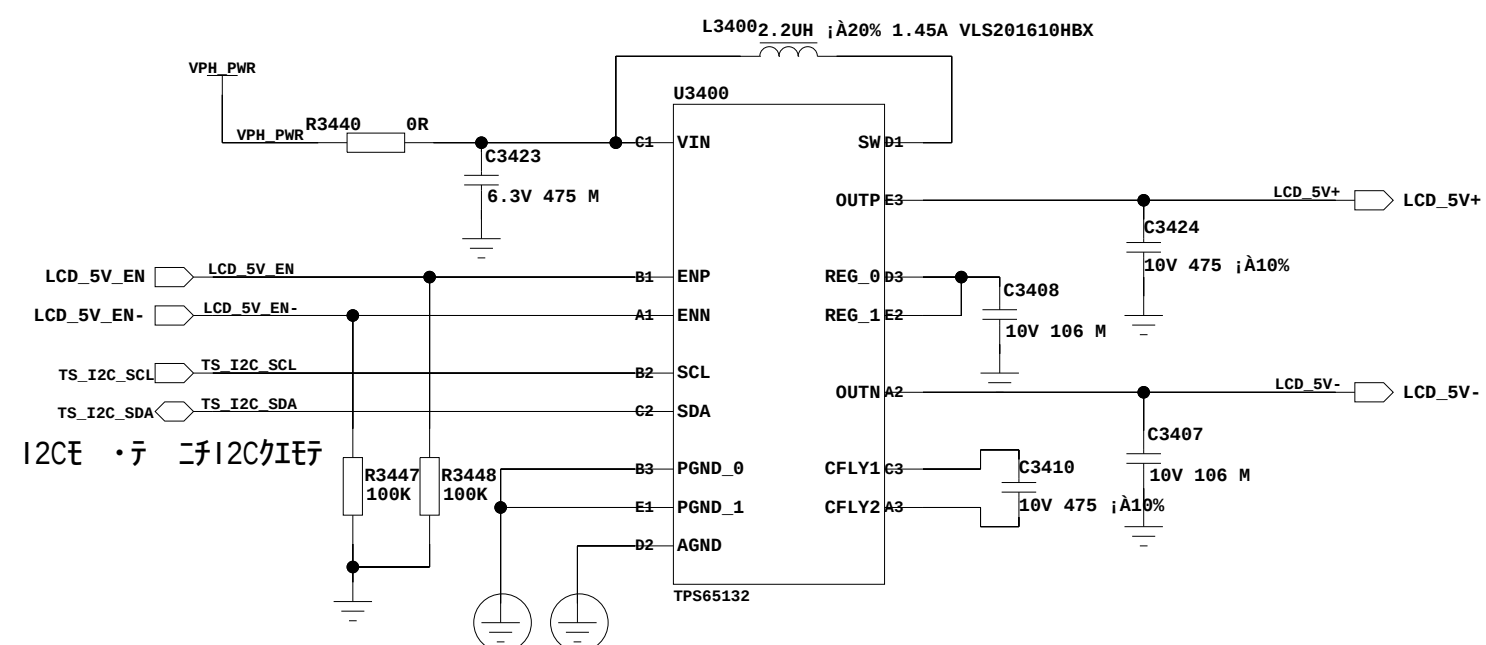
TP connector



LCM Connector

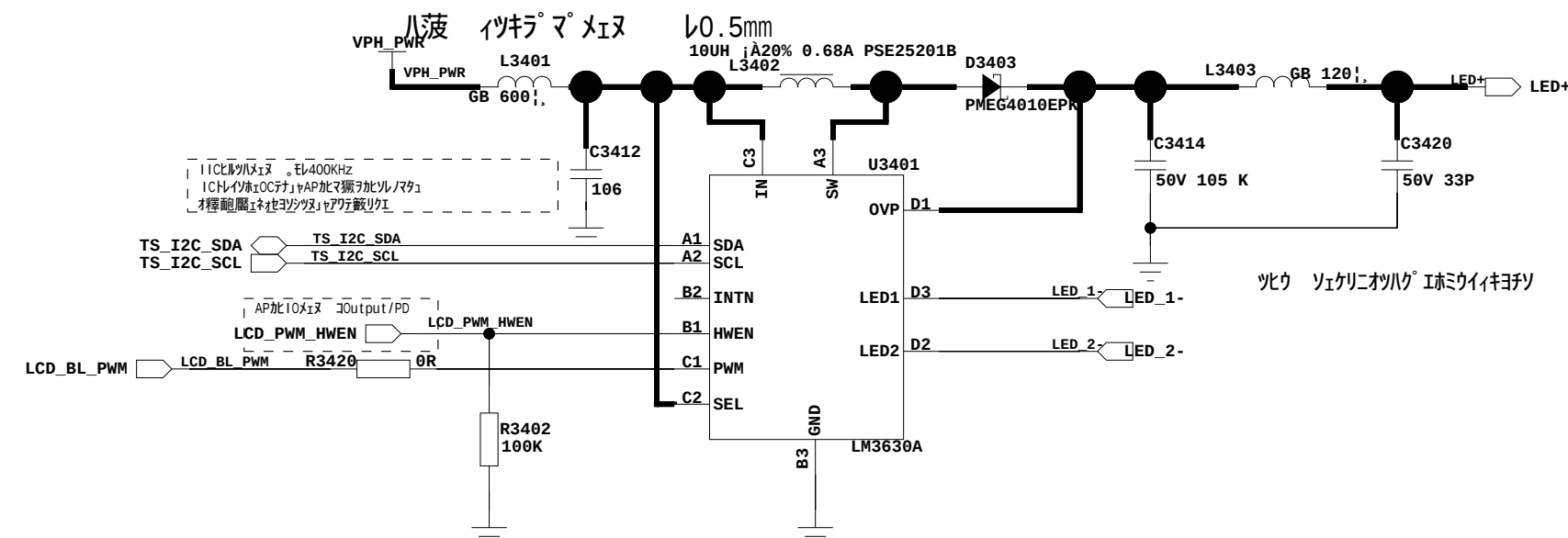


LCD 。 75V Driver



```
tps65132 slave address 0X3E
```

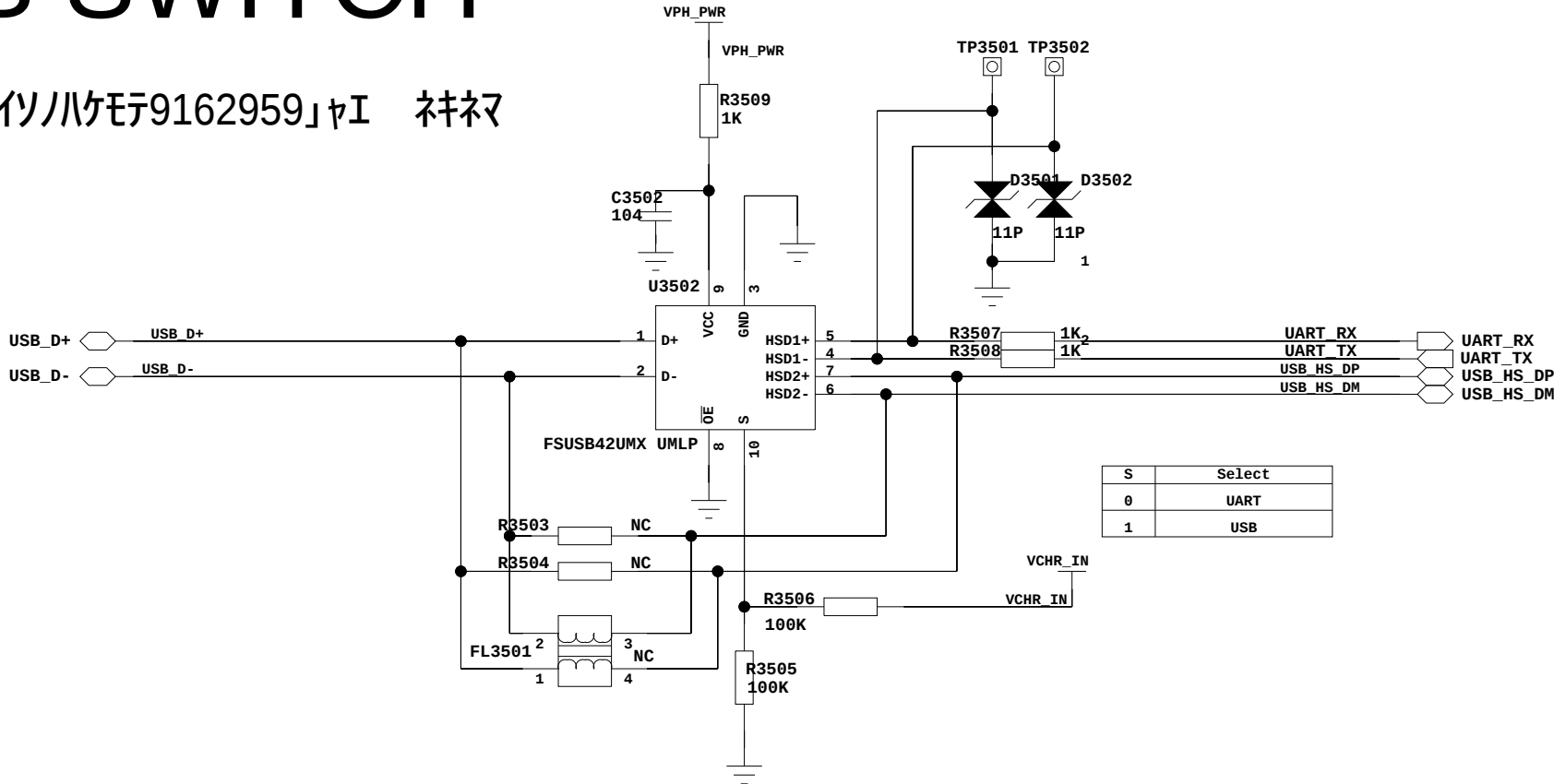
LM3630A slave address, 10x38



	Sign	Date	Type:		
Drawn By			Draw No: 15109		
Checked By			Sheet	of	26 Ver:
Standard By					
Approved By					

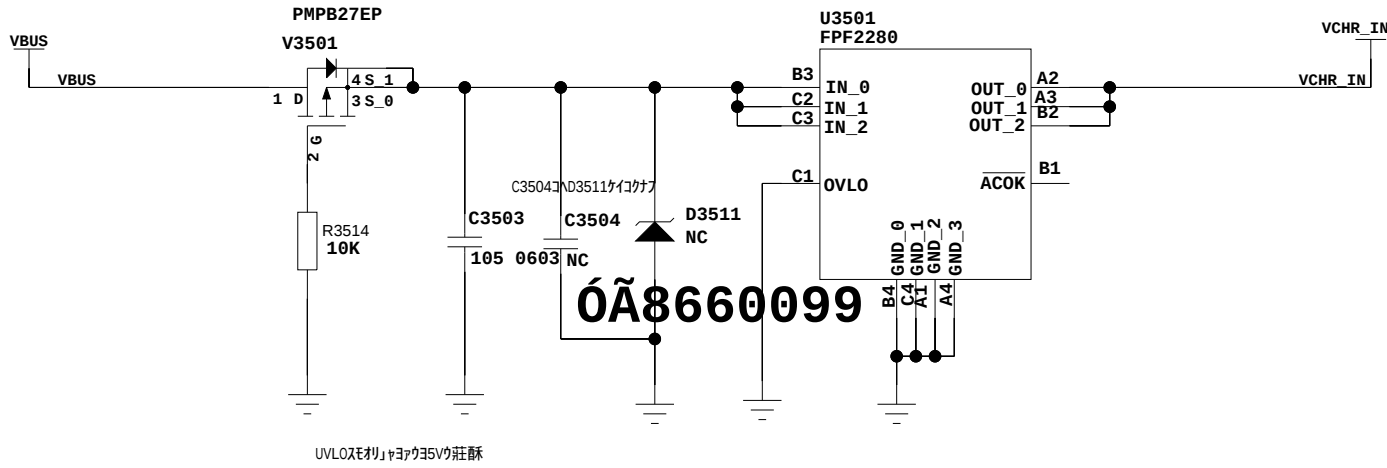
USB SWITCH

エレクトロニクス部品9162959「A」 部品

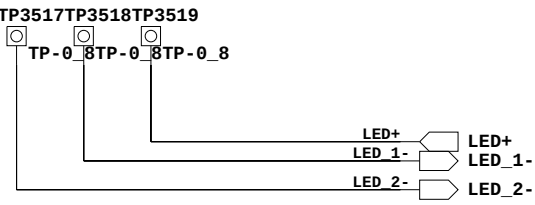
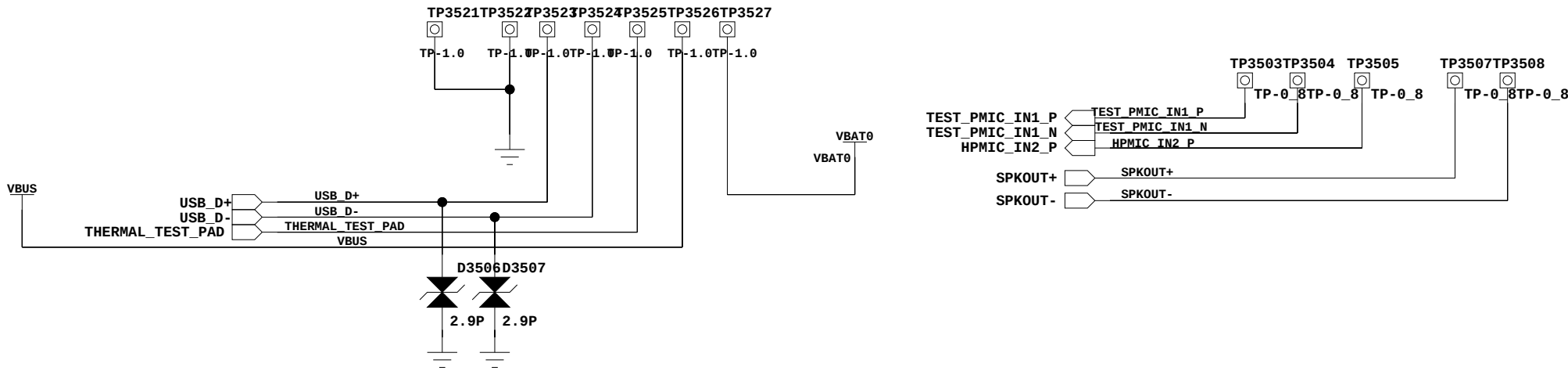


OVP

08080286



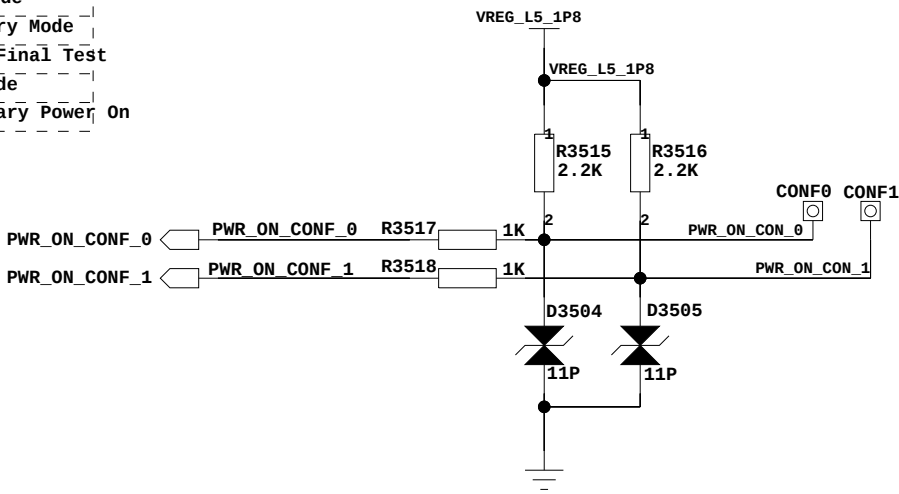
TEST POINT



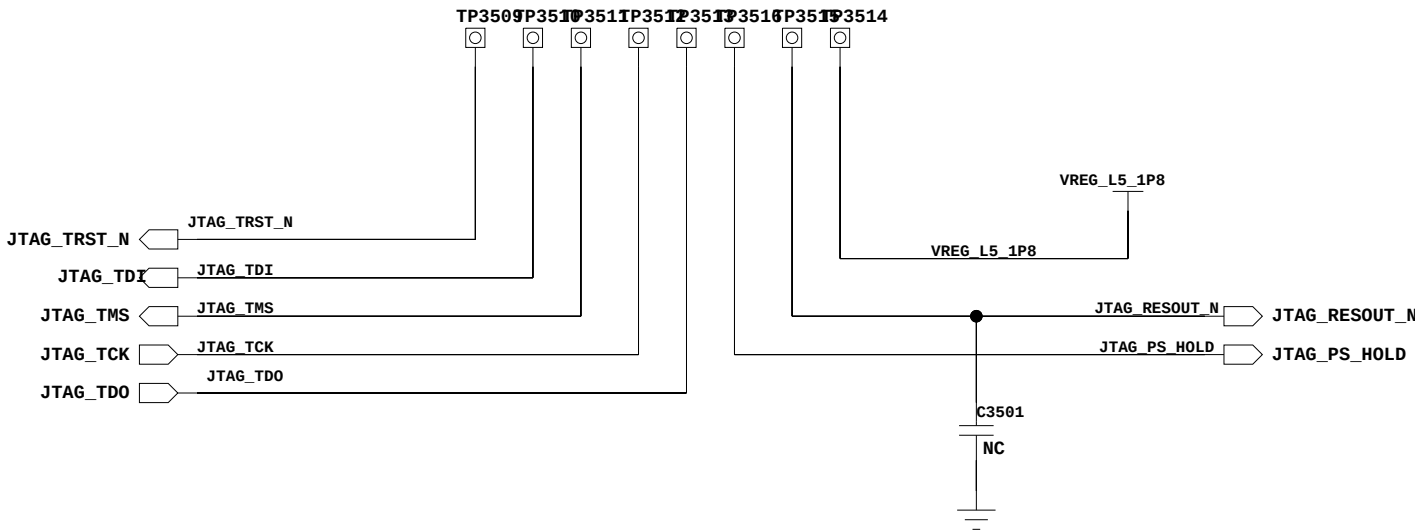
Test Confige

和イ電子部品

CONF_0	CONF_1	Mode
0	0	Factory Mode
0	1	WLAN Final Test
1	0	RF mode
1	1	Ordinary Power On

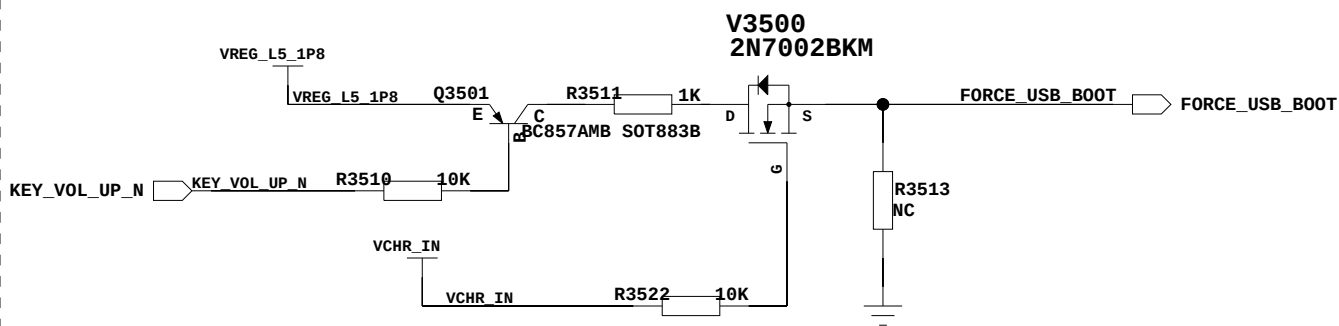


JTAG DEBUG



UPDATE

マツリト「ハイス」築材罐コ「オ」蘇スミミ



	Sign	Date	Type:
Drawn By			Draw No: 15109
Checked By			Sheet of 22 Ver:
Standard By			oppo
Approved By			

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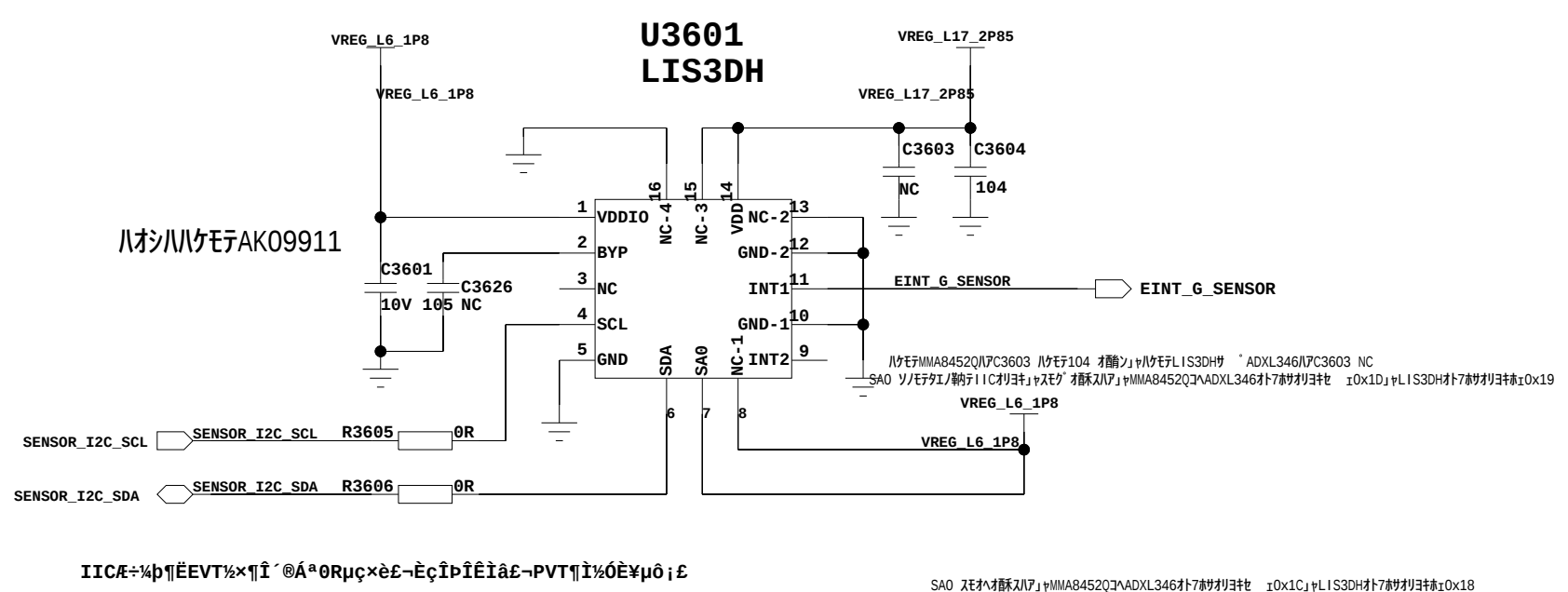
D

E

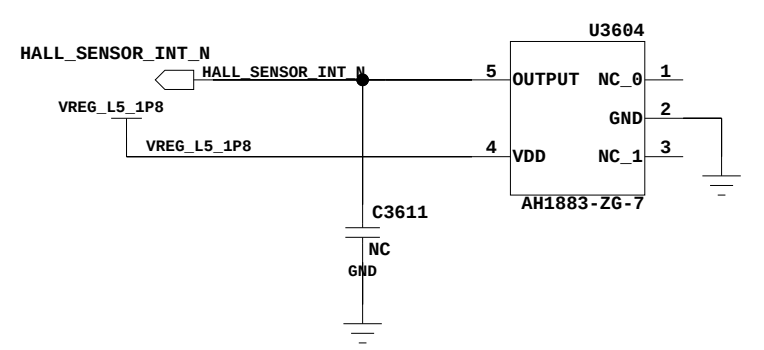
F

G-sensor

LIS3DH Slave address 0X19(0011001)

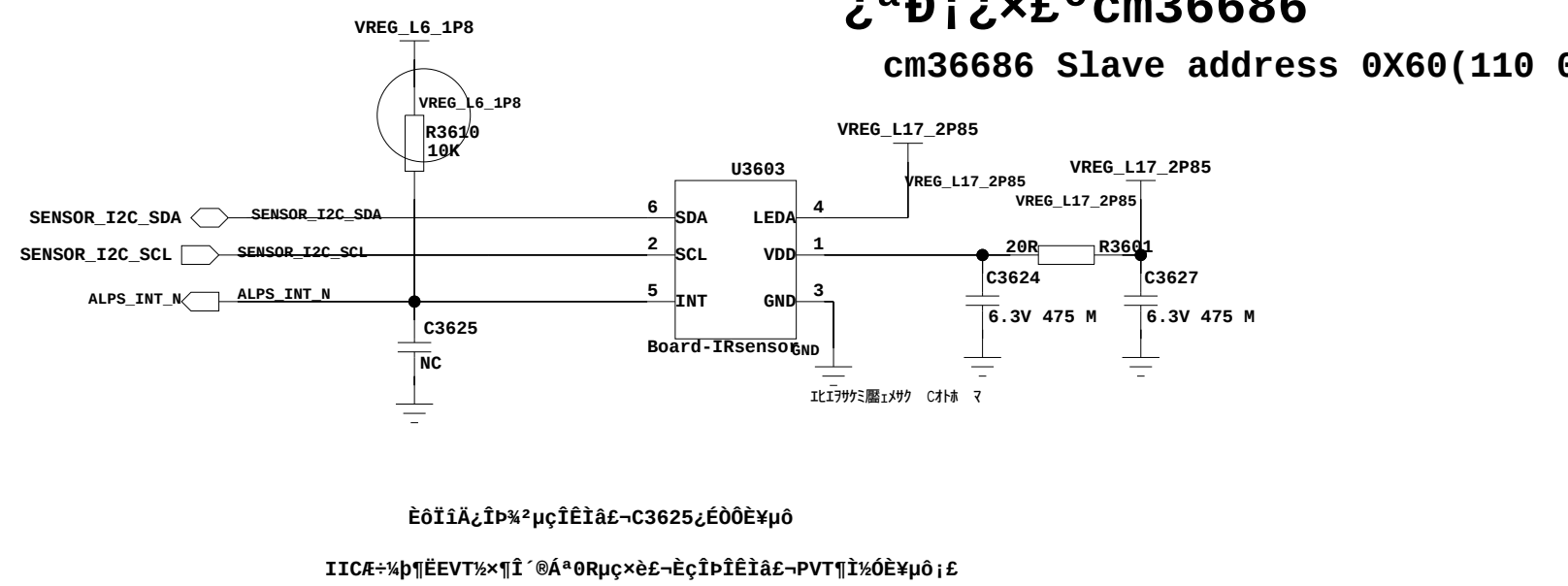


Hall sensor

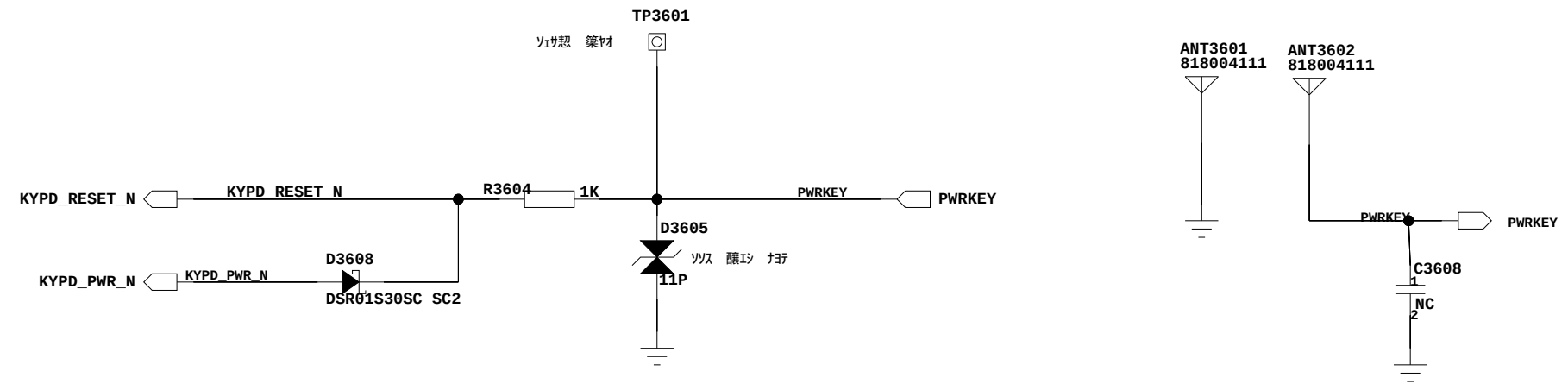


A.L.S.+P.S sensor module BTB

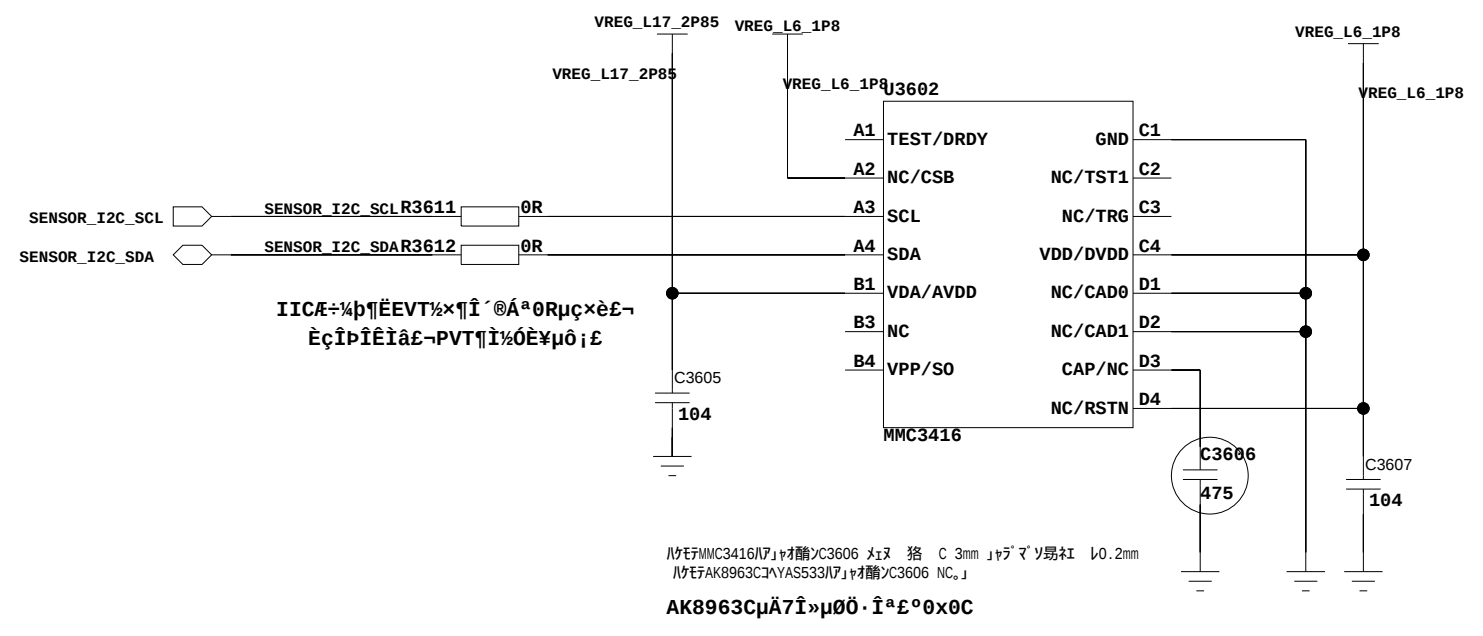
cm36686 Slave address 0X60(110 0000)



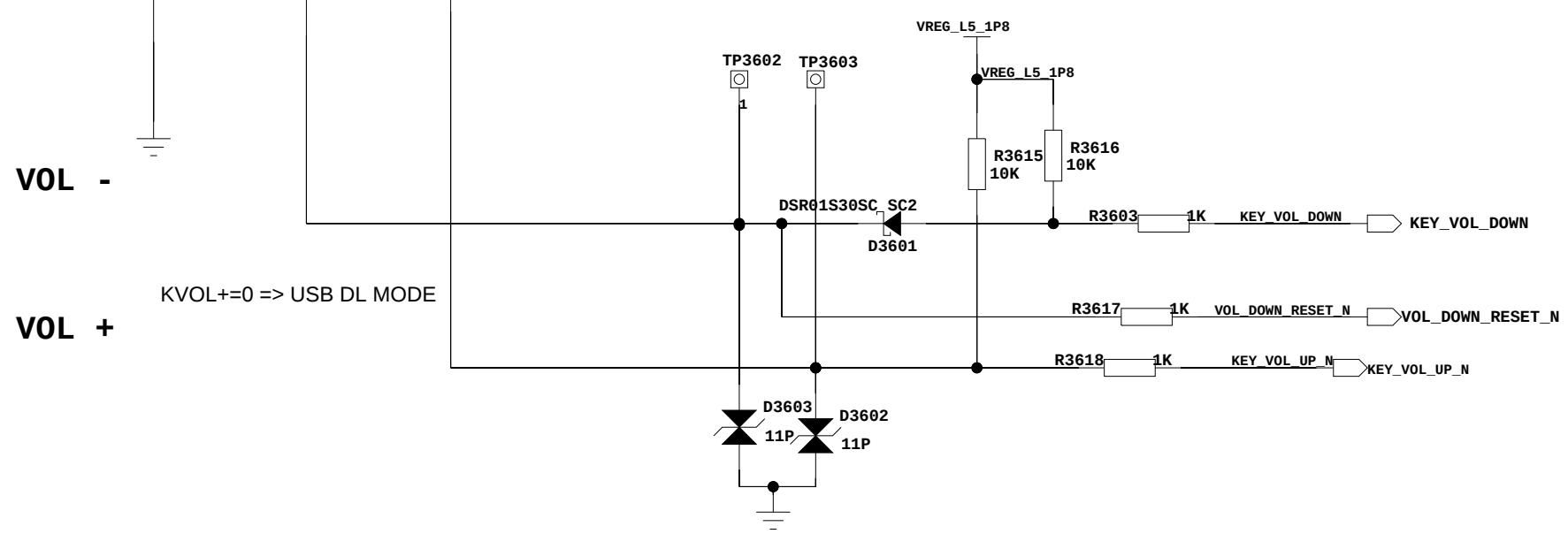
POWER KEY



M-sensor



VOL+/- KEY



	Sign	Date	Type: 15109
Drawn By			Draw No: 26
Checked By			Sheet 26 of Ver:
Standard By			
Approved By			



A

B

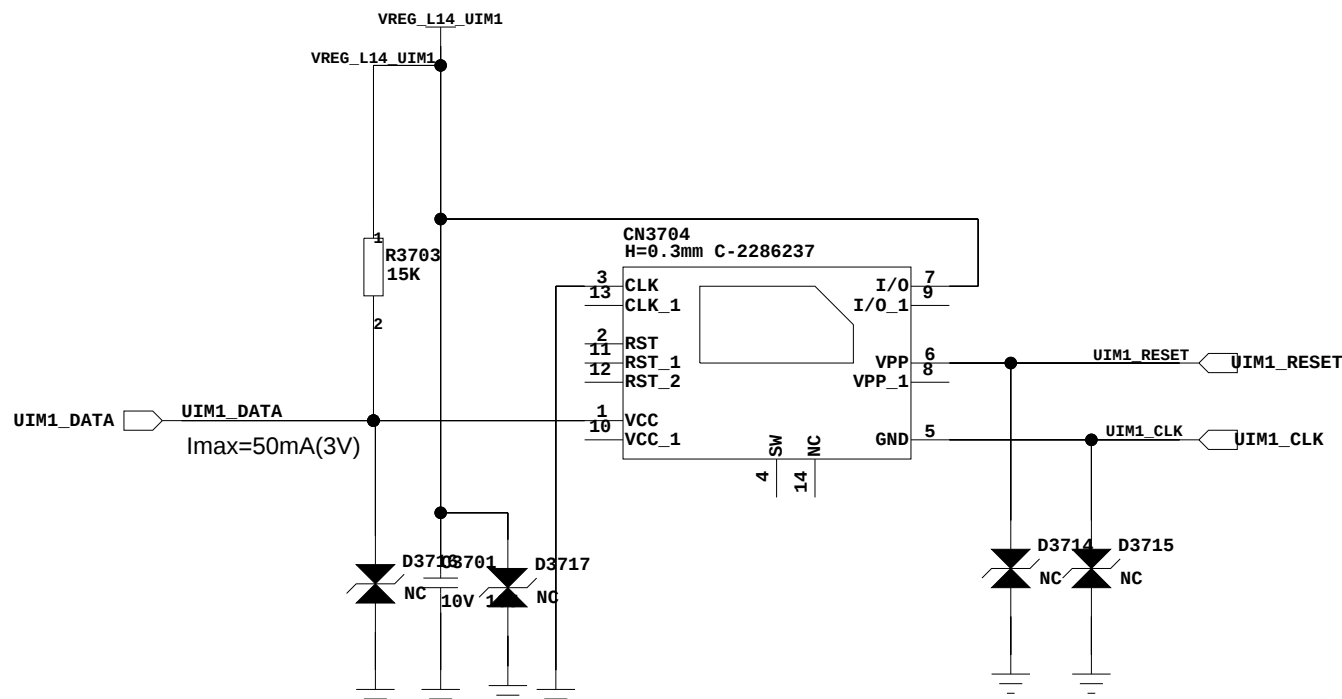
C

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E

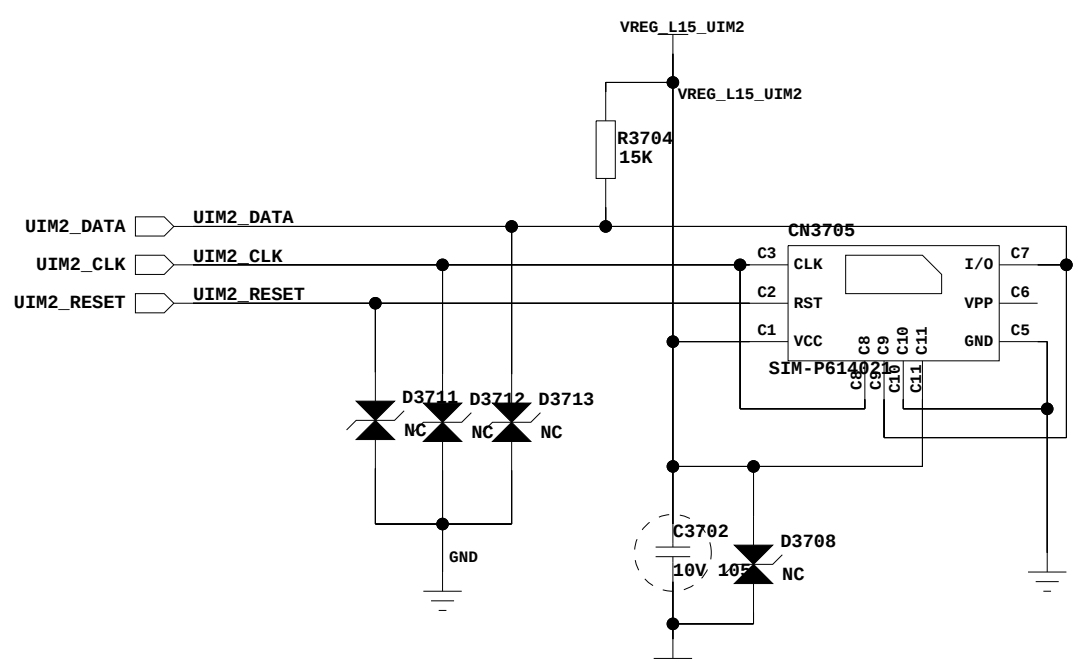
F

SIM1 Card



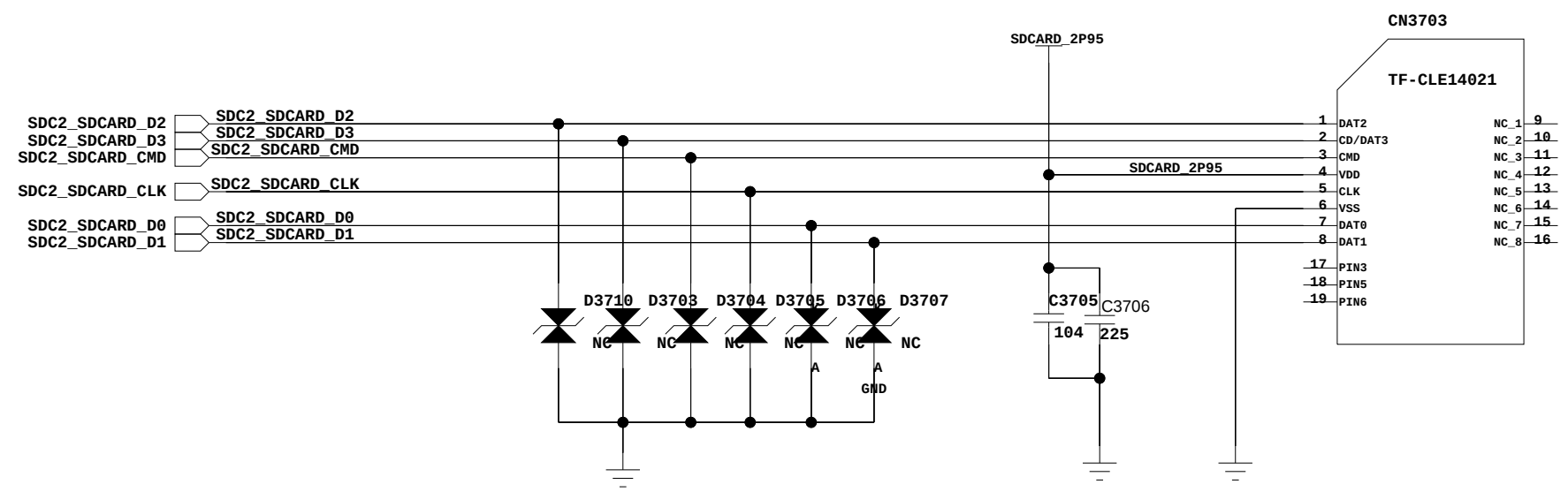
スライダスイッチpinは 部品納 旗-カメ衰横Lイヨミミカウニウ

SIM2 Card

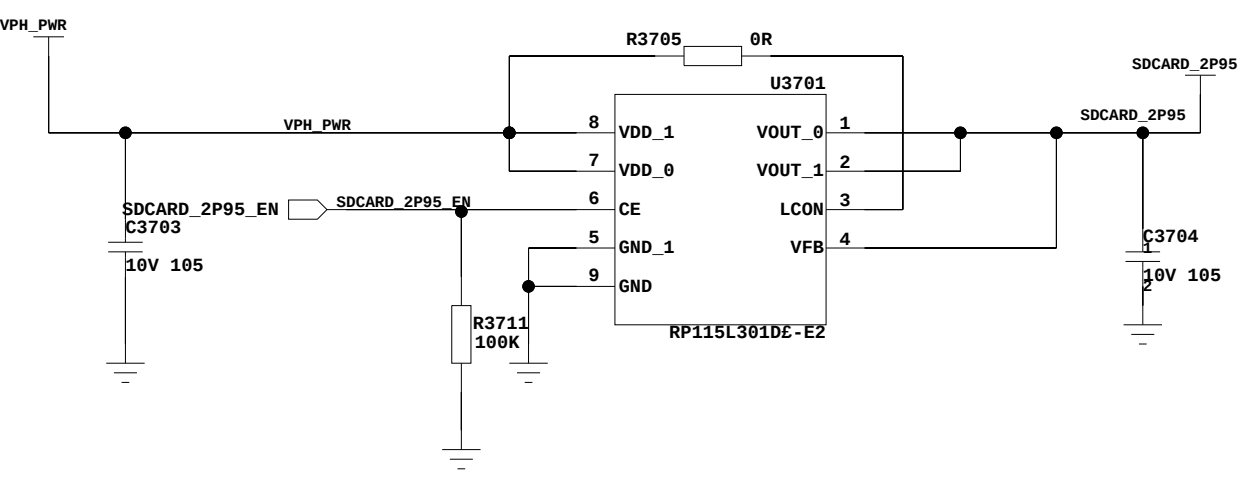


I_{max}=50mA(3V)

TF_CARD



T₂ 2.95V



	Sign	Date	Type:
Drawn By			Draw No: 15189
Checked By			Sheet of 29 Ver:
Standard By			oppo
Approved By			

A

B

C

D

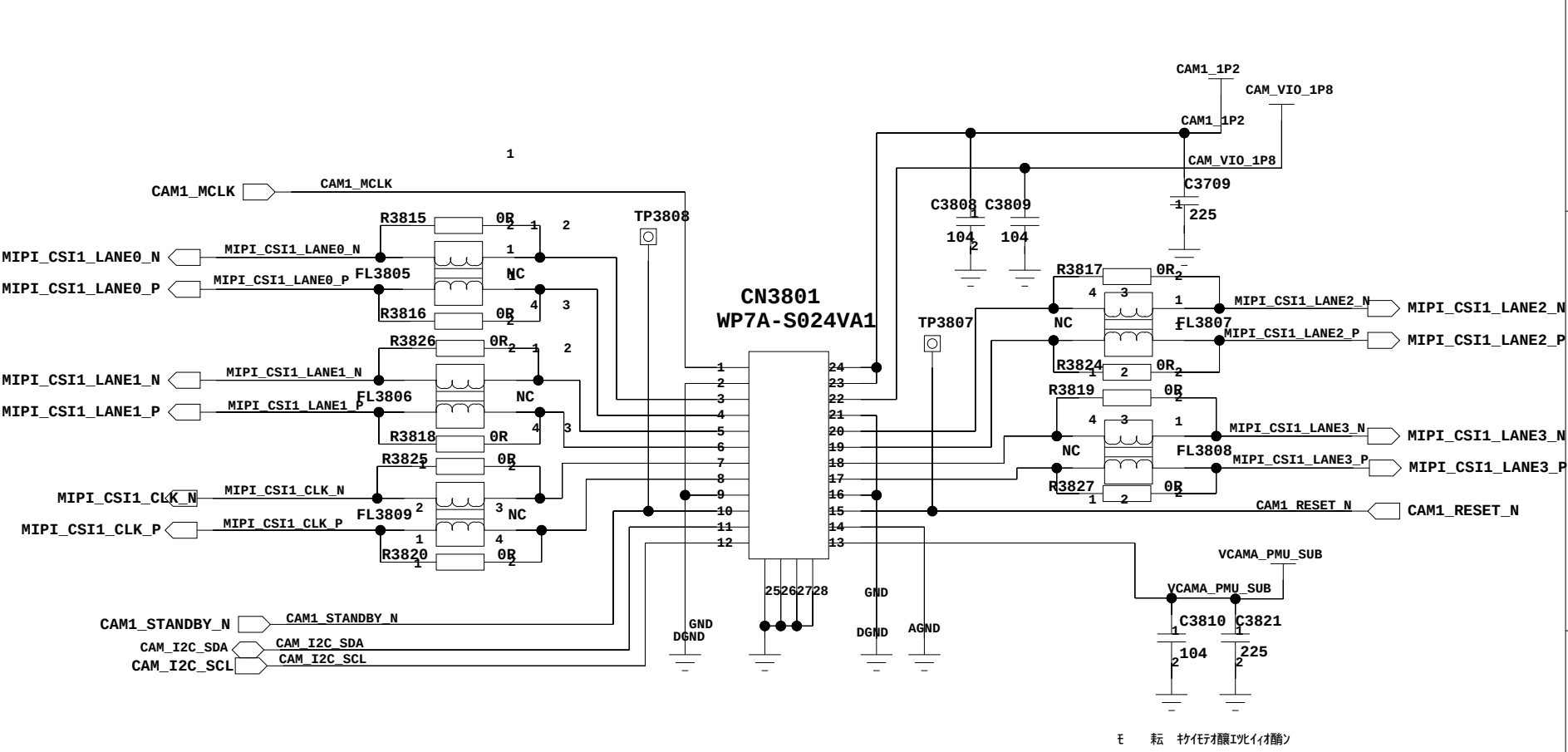
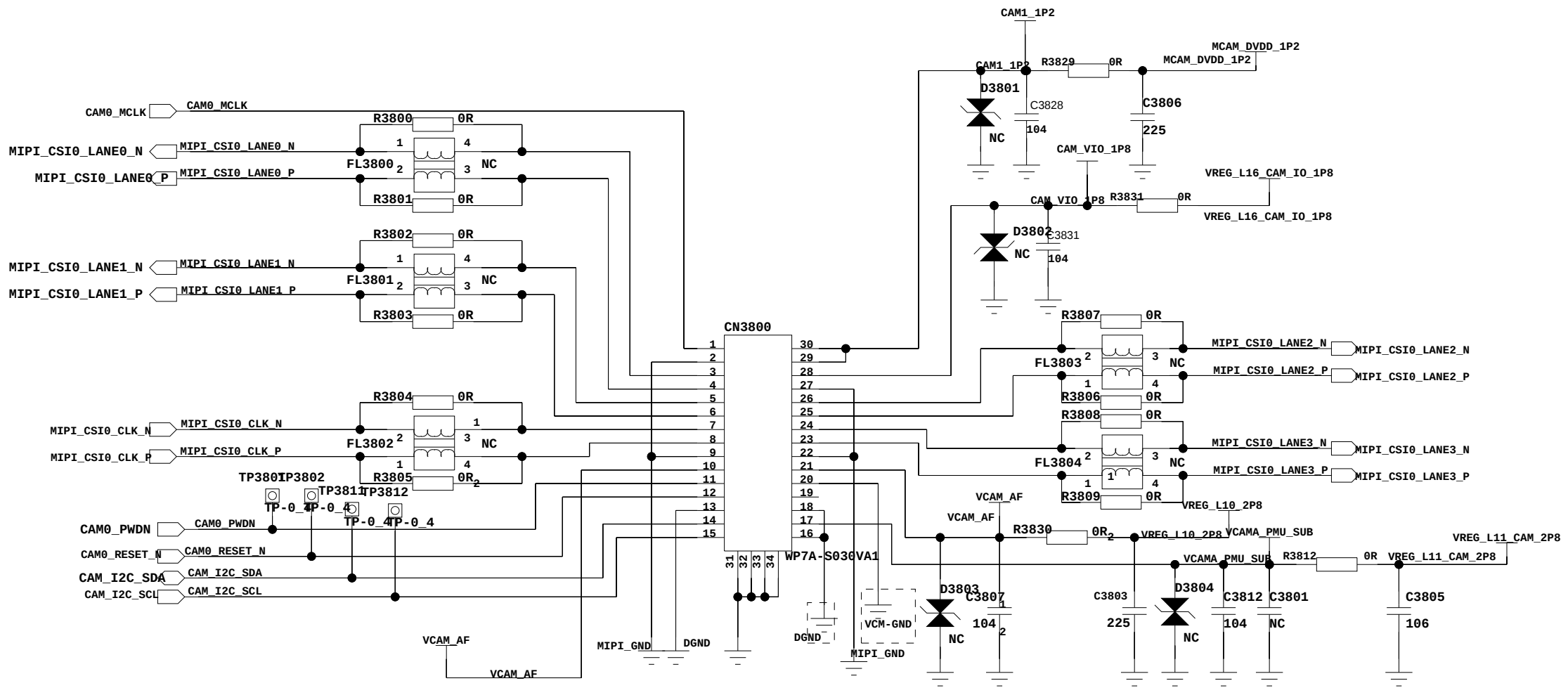
E

F

8

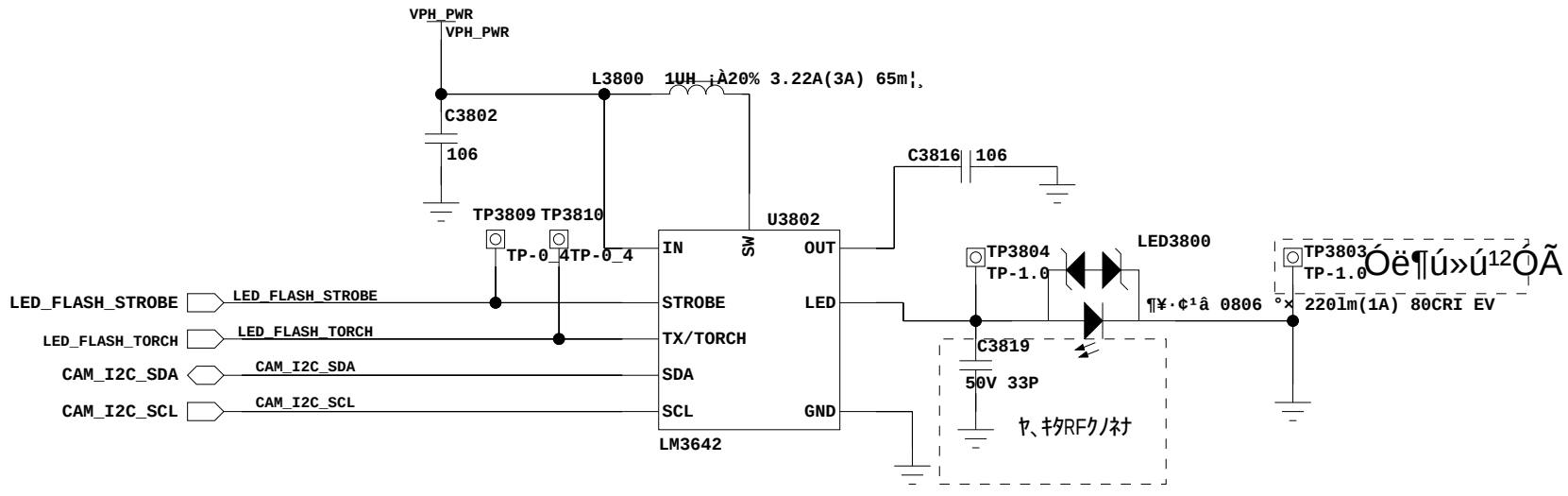
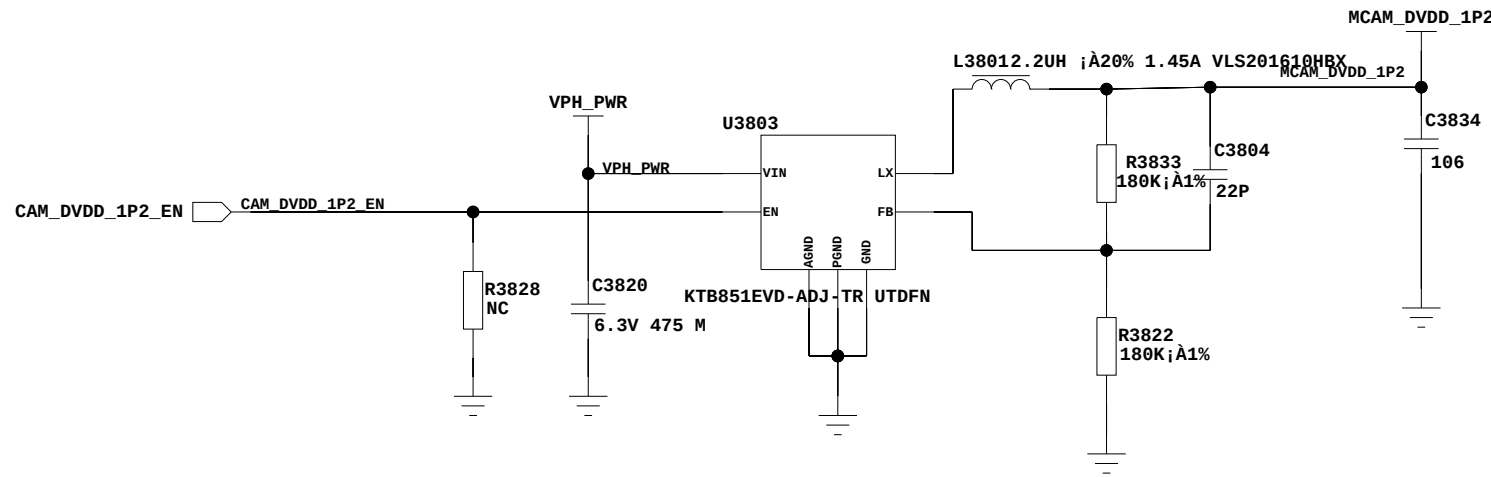
MAIN_Camera

SUB_Camera



SAMSANG 3L8 slave address 0X5A(W).0X5B(R)

Flash LED Driver



LM3642 slave address 0x63

